Abstract

We propose a network architecture to perform efficient scene understanding. This work presents three main novelties: the first is an Improved Guided Upsampling Module that can replace in toto the decoder part in common semantic segmentation networks. Our second contribution is the introduction of a new module based on spatial sampling to perform Instance Segmentation. It provides a very fast instance segmentation, needing only a thresholding as post-processing step at inference time. Finally, we propose a novel efficient network design that includes the new modules and test it against different datasets for outdoor scene understanding. To our knowledge, our network is one of the most efficient architectures for scene understanding published to date, furthermore being 8.6% more accurate than the fastest competitor on semantic segmentation and almost five times faster than the most efficient network for instance segmentation.

1. Introduction

Most of the current architectures that perform semantic segmentation rely on an encoder-decoder architecture. This is the simplest and most effective way to design the model in order to increase the receptive field of the network at the same time keeping the computational cost feasible. To mitigate the effect of information loss caused by downsampling, state-of-the-art architectures make use of additional ways to increase the receptive field, e.g. dilated convolutions [63], ASPP [11], PSP [65]. However the use of dilated convolutions results in computational heavy architectures and, in most cases, the best trade-off consists of a mix of downsampling and dilation. As a consequence, common network architectures, employ upsampling operators to output a semantic map with the same resolution as the input.

Our first contribution consists of a new module named Improved Guided Upsampling Module i.e. iGUM. It replaces traditional upsampling operators like bilinear and nearest neighbor and can be plugged into any existing architecture and trained end-to-end within the network. With a low additional computational cost, it helps to improve the prediction along object boundaries when upsampling output probability maps of semantic segmentation networks. We discovered that it can replace the entire decoder part of efficient semantic segmentation networks, obtaining a consistent speed-up and even an improvement of performance in most cases.

As our second contribution we designed a novel module that model Semantic Instance Segmentation as a diffusion process through a differentiable sampling operator. The resulting layer has three main advantages: It is computationally lightweight allowing for very fast inference at test time. It can be trained end-to-end with the whole network and requires only a thresholding as post-processing step.

Our third contribution consists of a novel lightweight neural architecture to perform scene understanding in applications where speed is a mandatory requirement. We run experiments to assess different aspects of our modules and our complete architecture on three different datasets: Camvid [7], PASCAL VOC 2012 [20] and Cityscapes [15]. Our network is able to achieve 68.9% of mIoU on the popular Cityscapes dataset at 113 FPS on a Titan Xp GPU being...
8.6% more accurate than the fastest published network up to date.

2. Related Works

Efficiency-oriented architectures SegNet [4], one of the first efficiency-oriented architectures together with [58], introduced an efficient way to exploit high-resolution information by saving max-pooling indices from the encoder and using them during upsampling. ENet authors [43] designed the first highly-efficient architecture by making use of clever design patterns and state-of-the-art building blocks: residual blocks [28], early downsampling, and 1D factorized convolutions [56]. We think that the use of factorized convolutions is the main reason for the success of this architecture. ERFNet [48] authors implemented an architecture with a very similar overall structure to ENet but with a residual module named Non-Bottleneck-1D module. The overall architecture raises the performance of ENet of a great margin. We will build our fast architecture on top of ERFNet. ESPNet [40] is a very recent, efficient architecture that makes use of a novel module named ESP module. It achieves a very good tradeoff in terms of accuracy and network speed.

Instance Segmentation architectures Following [19] we categorize instance segmentation architectures in four different classes: Proposal-based, Recurrent methods, Energy-based and Clustering. [24, 13] are examples of proposal-based approaches. They employ MCG [2] as a class-agnostic predictor of object proposals and they subsequently make use of a classification step to produce instances. A good number of recent works rely on the joint use of an object detector and a semantic segmentation network [17, 18, 3, 25]. As a matter of fact, recent state-of-the-art instance segmentation algorithms [36, 27] are based on enhanced versions of the Faster R-CNN object detector [22] but, from the point-of-view of this work, architectures like PANet [36] or Mask-RCNN [27], are still computationally too heavy to be employed on edge devices for real-time applications.

Other works like [54] fall into the category of recurrent methods, i.e. they adopt recurrent networks to generate instances in a sequential way. [49] uses LSTMs to output binary segmentation maps for each instance. [47] enhance the approach of [49] by adding an object detector network to refine the output. [5, 32] uses alternative ways to detect instances. [5] exploit the watershed transform. [32] uses an approach based on CRF together with a customized MultiCut algorithm. The last category of methods involves the transformation of the input image into a representation that is afterward clustered into a set of discrete instances [64]. [19] introduces a novel loss function that induces an embedding space representing separate instances. As a second step, it employs a clustering algorithm to extract the segmented instances. Instead [59, 30] trains a network to predict a dense vector field where each vector points towards the instance center. The fastest architectures for instance segmentation up to date belong to this category, see also Table 7. However, accurate clustering algorithms are quite heavy to run on a large set of points (pixels) with high-resolution images. This makes impractical to use this category of algorithms in real settings with real-time processing pipelines.

Our network architecture performs instance segmentation in a similar way to the clustering methods. However, it makes the use of a clustering algorithm unnecessary by exploiting a very simple and efficient iterative sampling algorithm. We will explain in details our method for instance
segmentation and outline the differences with [59, 30] in Section 4.

3. Guided Upsampling Module

Most state-of-the-art semantic segmentation networks are based on encoder-decoder architectures [12, 65, 11, 8, 62, 38, 43, 48, 40]. Since the task involves a dense per-pixel prediction, usually a probability vector over the classes distribution is predicted for each pixel. This is arguably inefficient both computationally and from a memory occupation point-of-view. A more efficient output representation would involve a non-uniform density grid which follows the objects density distribution in the scene. A step towards this direction has been presented in [39] with the introduction of the Guided Upsampling Module (GUM). GUM is an upsampling operator built to efficiently handle segmentation maps and improve them along objects’ boundaries. Classical upsampling operators (e.g. nearest neighbor or bilinear) make use of a regular grid to sample from the low-resolution image. GUM introduces a warping grid named Guidance Offset Table to correct the prediction map along object boundaries. The Guidance Offset Table is predicted by a neural network branch named Guidance Module. The whole module can be plugged-in in any existing architecture and trained end-to-end. Given \( V_i \) the output feature map and \( U_{nm} \) the input feature map, GUM over the nearest neighbor operator can be defined as follows:

\[
V_i = \sum_{n}^{H} \sum_{m}^{W} U_{nm}(\delta([x_i^s + p_i + 0.5] - m) - \delta([y_i^s + q_i + 0.5] - n))
\]

where \( x_i^s \) and \( y_i^s \) are the spatial sampling coordinates. \( [x_i^s + 0.5] \) rounds coordinates to the nearest integer location and \( \delta \) is a Kronecker function. \( p_i \) and \( q_i \) are what makes GUM different from nearest-neighbor: two offsets that shifts the sampling coordinates of each grid element in \( x \) and \( y \) dimensions respectively. They are the output of a function \( \phi_i \) of \( i \), the Guidance Module, defined as: \( \phi_i = (p_i, q_i) \). For the definition of GUM over bilinear sampling refer to [39]. The resulting operator is differentiable with respect to \( U, p_i \) and \( q_i \). In [39], the GUM module is applied to the network output probability map, even though it could be employed anywhere within the architecture.

3.1. Improved Guided Upsampling Module

Let \( U_{cnm} \in \mathbb{R}^{C \times N \times M} \) be an output probability map to be upsampled, where \( C \) represents the number of classes, \( N \) and \( M \) are the spatial dimensions. The GUM module needs a Guidance Offsets Table of the same spatial dimensionality as the output features map. Let the upsampling factor be \( \tilde{f} \). To produce a feature map \( V_{cnm} \in \mathbb{R}^{C \times \tilde{f}N \times \tilde{f}M} \) the GUM needs to predict a guidance offsets table of cardinality \( 2 \times fN \times fM \). This is certainly more efficient than predicting the full resolution probability map, especially if \( C \) is large because, regardless of the cardinality of classes, the network has to predict only a 2-dimensional vector (offset) for each pixel in the output map. However, a lightweight decoder structure is still needed. Our desiderata are to reduce even more the computational burden and to completely remove the decoder part of the network.

A geometric view on GUM Figure 3 depicts a toy example on the use of GUM to upsample an output probability map. The spatial dimension is visualized on the x-axis. In this example, only one dimension is represented although in the real problem there are two spatial dimensions. On the y-axis is represented the probability over the two classes. The two dashed vertical lines indicate two points in the low-resolution probability map. Pairs of black points lying on these lines, i.e. (A1, A2) and (B1, B2) represent the predicted probability vector over the two classes (1 and 2). The continuous black lines represent the sub-pixel values of the probability distribution obtained by linear interpolation. Notice that, this visualization in a continuous space, allow us to abstract from the upsampling factor. A gray vertical line is depicted on the argmax boundary. i.e. the point where the probability distribution is equal over the two classes and where the argmax value changes. By looking at Figure 3 it is clear that the position of the original boundary is dependent on the values of the four points A1, A2, B1, B2. Thus, the boundary subpixel position is tied to the two adjacent probability vectors. The idea of GUM is represented in Figure 3 by the multiple thin parallel orange arrows. They are spatial sampling vectors predicted by the network at high resolution. The probability value is sampled, i.e. copied, from the head to the tail of each ar-
row. With this sampling operation, it is possible to move the \textit{argmax} boundary as shown in Figure 3. Notice that the orange parallel arrows are the same for the two classes: i.e. the boundary can be moved by predicting the same arrows for every class.

**Improved GUM intuition** Our intuition is that the \textit{argmax} boundary can be moved by predicting only the low-resolution sampling offsets, i.e. the red arrows at the bottom of Figure 3. (The other bold red arrows are the same arrows moved along the vertical axis). By looking at the parallel thin arrows in between, we observe that they can be obtained by interpolating the bold arrows. We can move the \textit{argmax} boundary by predicting only one additional value low-resolution pixel. Intuitively we can extend this concept to two dimensions by predicting a 2D spatial offsets vector for each spatial location and obtain the other sampling offsets by bilinear interpolation. To summarize, a network with the Improved Guided Upsampling Module, results in a very simple structure. It is composed by an encoder with two branches: the first predicts the output probability map for each spatial location and obtain the other sampling offsets by bilinear interpolation.

4. Instance Segmentation Module

The most efficient architectures for instance segmentation e.g. [30, 41] (see Section 2) are trained to produce a dense output map with a particular form of embedding for each pixel. To extract every single instance, the embedding needs to be post-processed by a clustering algorithm. Usually, the time needed to run the clustering algorithm on the raw output is not considered when evaluating the speed of state-of-the-art methods. As a matter of fact, running a clustering algorithm on a high-resolution output is computationally intensive, making these approaches inefficient in real-world scenarios. Consider for example the mean-shift algorithm [14] employed by [19]. It has a complexity of $O(Tn^2)$ where $T$ is the number of iterations and $n$ is the number of points in the data set. In case of Cityscapes dataset where the resolution of the images is $1024 \times 2048$ the number of points is 2 Million and the number of operations can easily scale up to 20 G-FLOPs, which is roughly the amount of operations needed to perform a forward pass on a VGG-19 architecture [51] (one of the heaviest architectures to date [6]).

**Instance Segmentation by Sampling** We propose an instance segmentation module that associates to each pixel the instance centroid, similarly to [30, 41]. We train our
network with an $L^2$ regression loss applied directly to the network output:

$$
\mathcal{L}_{\text{Instance}} = \frac{1}{N} \sum_{n \in \mathcal{N}} \| i_n - \hat{i}_n \|_2^2
$$

where $i \in \mathbb{R}^2$ is the instance center in pixel coordinates, $\hat{i}_n \in \mathbb{R}^2$ is the vector predicted by the network. The loss is averaged over every labeled pixel $n \in \mathcal{N}$ in a minibatch. The peculiar trait of our method lies in the architecture. Figure 4 shows how the Instance Segmentation module works: a Guidance Module predicts a Guidance Offsets Table of vectors (bearing the terminology from [39]). The usual way to train instance segmentation networks [30] is to directly apply the loss function in Eq. 2 to the Guidance Offsets Table. In our architecture instead, a differentiable sampler, like the one used in [39] or [29], samples 2D points from a fixed regular grid using the Guidance Offsets Table. The fixed regular grid codifies the 2D coordinates of the exact location of each pixel. This sampling process is applied $t$ times sharing the same Guidance Offsets Table (see Figure 4). Thus, by consecutive sampling steps, the 2D coordinate values of instance centers are spread all over the area covered by the instance. Finally, the loss function in Eq. 2 is computed on the sampled output. Figure 4 shows the gradient flow: notice that, it flows backward through every sampler step but not to the fixed regular grid. The only way to decrease the loss function is to produce a Guidance Offsets Table with vectors that point towards the instance center.

There is a main advantage of this approach over the classical method of training directly the vector field: in an ideal setting, if the network predicts a perfect output, all the vectors associated with a particular instance will point precisely to the instance center. This doesn’t happen in practical cases, thus, the need of a clustering algorithm. Here emerges the major advantage of our approach: by sampling multiple times, a diffusion process is generated. As a consequence, the value of the instance center propagates through all the pixels associated with that particular instance. If a far vector points to an imprecise location towards the instance center, by successive sampling steps, the center value propagates to increasing areas eventually covering all the instance area. Top visualization in Figure 6 shows the Guidance Offsets Table overimposed on the input image. Note that the vectors’ magnitude decrease drastically near the instance center. The visualization at the bottom of Figure 6 is obtained by summing for each pixel location the 2D centroids’ predictions coordinates. A single value per location is obtained (it can be interpreted as a unique instance identifier). In Figure 6 a different color is assigned to each unique value. The only post-processing step applied to the final output is a thresholding over the instance area. Very small false positives are eliminated by this step, they can be noticed by zooming Figure 6 (bottom) along object boundary.

Two Practical Tips First, to simplify the learning process, the values of the Guidance Offsets Tables both for the Improved GUM and for the Instance Module are limited to the interval $[-1, 1]$ by mean of a $\tanh(x) = \frac{e^x - e^{-x}}{e^x + e^{-x}}$ function. Second, the GUM operator used to upsample the instance output is based on Bilinear sampling at training time, and Nearest-neighbor at test time. Nearest-neighbor is the right choice to upsample unique id values but it is not directly differentiable w.r.t the Guidance Offsets Table.

5. Network Architecture

Our architecture for semantic segmentation consists of a lightweight encoder and an iGUM layer as decoder. For the task of instance segmentation we added the Instance Segmentation Module before the guided upsampling (refer to Figure 2 for details). The iGUM is described in details in Section 3 whereas the encoder has a very simple structure inspired by [48]. However, the main building block is a novel Lightweight Non-bottleneck Module described in this section.

Encoder-only architecture In encoder-decoder architectures, the decoder plays a refinement role where features are subsequently upsampled to match the input size and to finally output the dense per-pixel prediction. By employing our iGUM module we are able to completely remove the decoder part: as shown in Table 1 this a major factor for a highly-efficient semantic segmentation network.

Lightweight Non-Bottleneck-1D Module The Non-Bottleneck-1D module has been proposed by Romera et al. as the main building block of ERFNet [48]. It is a residual block composed of 1D convolutions also known as Asymmetric convolutions [37]. Numerous works investigated decomposed filters from a theoretical point of view, e.g. [52, 1]) and in practical settings, e.g. [57, 55, 43, 37]. The idea is that each convolutional layer can be decomposed in 1D filters that can additionally include a non-linearity in between. The decomposed layers have intrinsically lower computational cost than the corresponding full rank counterparts. Our Lightweight Non-Bottleneck Module consists in a very simple design: it is a non-bottleneck residual block with two asymmetric kernels $(3 \times 1)$ and $(1 \times 3)$ preceded and followed by $1 \times 1$ channelwise convolutions. The module design is depicted in Figure 5. We discovered this module to be particularly effective as part of our architecture. We motivate every design choice experimentally within the ablation study in Section 6.1.

Early downsampling Following the last works on efficient models [43, 63, 48, 46] our architecture employs an early downsampling strategy to speed-up inference time. In our network, the first layers act as early feature extractors whereas the most intense operations are carried out by the inner network modules to favor a more complex representa-
tion in late activations. Our downsampling block, inspired by [48, 43], performs downsampling by concatenating the parallel outputs of a single $3 \times 3$ convolution with stride 2 and a Max-Pooling module.

**6. Experiments**

We built our architecture based on different types of experiments to verify each design hypothesis. After presenting the implementation details for the sake of reproducibility, we introduce different experiments concerning the Improved Guided Upsampling Module and in-depth ablation studies on our architecture. Finally, we compare our network with state-of-the-art efficient architectures on different datasets for semantic and instance segmentation.

**Evaluation metrics** mean of class-wise Intersection over Union (mIoU) is used to measure semantic segmentation quality on all datasets. It is computed as the classwise mean of the intersection over union measure. On Camvid dataset also the class average and the global average are computed being the mean of the accuracy on all classes and the global pixel accuracy respectively. Frame Per Second (FPS) is used as speed measure, defined as the inverse of time needed for our network to perform a single forward pass. FPS have been computed on a single Titan Xp GPU whether not differently specified. Following [43], we removed all Batch-Normalizations at test time merging them with close convolutions.

**Training Recipes** All experiments have been conducted within the Pytorch framework [44] v1.0. For training, following [48] we use the Adam optimizer [31] in an initial learning rate of $5e^{-4}$ and weight decay of $1e^{-4}$. The learning rate is scheduled by multiplying the initial learning rate by $(1 - \frac{e}{\text{maxEpochs}})^{0.9}$. All models are trained for 150 epochs with a mini-batch size of 8. We also include Dropout [33] in all our Lightweight Non-Bottleneck modules as regularizers. Following [48] we set the dropout rate of the first five modules to 0.03 and all the others to 0.3.

### 6.1. Results on Cityscapes

Cityscapes [15] is a large scale dataset for semantic urban scene understanding. It consists of 5000 finely annotated high-resolution images with pixel-level fine annotations. Images have been collected in 50 different cities around Europe, with high variability of weather conditions and in different seasons. We used the standard split suggested by the authors which consist in 2975, 500, and 1525 images for train, validation, and test sets respectively. Annotations include 19 classes used to train and evaluate models. Following a common practice for efficient oriented architectures [48, 43, 40], images have been subsampled by a factor 2 for every experiment reported on Cityscapes dataset.

### Table 1. mIoU and FPS on Cityscapes val set by replacing the original decoder with our Improved Guidance Upsampling Module in GUNet and three other efficient architectures for semantic segmentation.

<table>
<thead>
<tr>
<th>Encoder</th>
<th>Pretraining</th>
<th>Decoder</th>
<th>mIoU (%)</th>
<th>FPS</th>
</tr>
</thead>
<tbody>
<tr>
<td>ERFNet (baseline)</td>
<td>✓</td>
<td>ERFNet</td>
<td>72.3</td>
<td>60.3</td>
</tr>
<tr>
<td>ERFNet</td>
<td>✓</td>
<td>improved GUM</td>
<td>71.6</td>
<td>84.4</td>
</tr>
<tr>
<td>Ours</td>
<td>✓</td>
<td>improved GUM</td>
<td>69.3</td>
<td>113.1</td>
</tr>
</tbody>
</table>

**Improved GUM on efficient architectures** We investigated the use of iGUM module on GUNet [39] as a replacement for the original GUM module. Furthermore, we tested our iGUM within three state-of-the-art efficient architectures for semantic segmentation. We trained the networks with their original decoder and compared them with modified versions where the decoder is replaced by the improved GUM module. Table 1 shows the results of these experiments: the first line shows the performance of GUM architecture with the original GUM and with our iGUM module. The results support the theoretical study exposed in Section 3.1: by replacing GUM with iGUM in GUNet architecture [39], the loss in accuracy is negligible. On the other hand, the model exhibits a visible benefit in speed. Both speed and mIoU improve over the baselines on all the other architectures by a large margin. The baseline does not correspond exactly with results reported by the papers because we trained these three networks with the same settings exposed in Section 6 which may differ from those used by the authors to train their own architecture. Moreover, they have not been pretrained and the speed has been evaluated by removing the Batch-Normalization layer. For the sake of these experiments, we are only interested in the relative performances.

**Ablation Studies** We designed our network starting from ERFNet [48] as baseline. First, we replaced the decoder part, which in the original ERFNet is composed of three deconvolutions and four Non-Bottleneck-1D Modules. The introduction of the Improved GUM cause a negligible performance decrease, i.e. from 72.3 to 71.6 but improves speed by 24 frames per seconds. As a second step, we replaced also the encoder obtaining a completely new architecture. Again, a small decrease in performance but a
large speedup compared to the baseline. Table 2 shows the results of the two ablation experiments.

In Table 3 we show the results of the ablation experiments for every choice made to obtain the Lightweight Non-Bottleneck-1D module. The baseline is an architecture composed by Non-bt-1D modules from [48]. A Non-bt-1D module is composed by four factorized 1D kernels. The first two have dilation term 1 whereas the last two have different dilation terms, i.e from 1 to 16, depending on the position within the architecture. We replaced the first two factorized convolutions with 1x1 convolutions. This speeded up the architecture by 15 FPS with a negligible mIoU decrease. Then we removed the biases from every convolution obtaining an mIoU of 63.6% and a slight increase in speed. Inspired by [43] we replaced ReLUs with PReLUs. This increased mIoU by 2% without any loss in speed. By applying these modifications together we got a mIoU of 63% and a very fast encoder, i.e. 113.1 FPS. Finally we moved one 1x1 convolution to the end of the residual module like shown in Figure 5 obtaining an mIoU of 64.1%. Last row shows the effect of pretrained on ImageNet.

**Instance Segmentation** With the first cluster of experiments we want to determine the best loss function to train our architecture for the instance segmentation task. Table 5 shows results on the Cityscapes validation set in terms of mIoU on semantic segmentation and AP on instance segmentation. Surprisingly the variance is low between different loss functions. We decided to keep \( L^2 \) loss for the next experiments. With our second cluster of experiments we want to assess how many sampling iterations \( t \) (see Section 4) are needed for convergence and how much they affect performance and speed. Table 6 shows the results for these experiments. The FPS are not very affected, due to the efficiency of the sampling module, the performances start to degrade significantly from 15 iterations. We decided to keep 30 iterations for the next experiment. We tested the network against the Cityscapes test set: results are shown in Table 7. Our method achieve the 9.2% of AP which is far from the state-of-the-art accuracy-oriented methods like PANet [36] but it exhibit a remarkably fast inference time.

### 6.2. Results on Camvid

We tested our architecture on the Camvid[7] dataset. It is composed of 367 training and 233 testing images of urban outdoor environments. It has been tagged in eleven semantic classes of which one is not evaluated and thus not used for training. The original frame resolution is 970 × 720. Fol-
Table 8. Results on Camvid test set ordered by increasing mIoU. Our model outperform every other efficient architecture by a large margin. It even yields better results with respect to some accuracy-oriented architectures.

<table>
<thead>
<tr>
<th>Method</th>
<th>Pretraining</th>
<th>Class avg. mIoU</th>
<th>Global avg. mIoU</th>
</tr>
</thead>
<tbody>
<tr>
<td>SegNet [4]</td>
<td>✓</td>
<td>65.2</td>
<td>55.6</td>
</tr>
<tr>
<td>ENet [23]</td>
<td></td>
<td>68.3</td>
<td>51.3</td>
</tr>
<tr>
<td>ESPNet [40]</td>
<td></td>
<td>68.3</td>
<td>55.6</td>
</tr>
<tr>
<td>ERFNet [43]</td>
<td>✓</td>
<td>72.5</td>
<td>62.7</td>
</tr>
<tr>
<td>FCN-8s [38]</td>
<td>✓</td>
<td>n/a</td>
<td>57.0</td>
</tr>
<tr>
<td>Dilation8</td>
<td>✓</td>
<td>65.8</td>
<td>61.6</td>
</tr>
<tr>
<td>Ours</td>
<td>✓</td>
<td>76.9</td>
<td>68.7</td>
</tr>
</tbody>
</table>

Table 9. Results on PASCAL VOC 2012 test set. We reported some popular methods and the state-of-the-art on Pascal VOC.

<table>
<thead>
<tr>
<th>Method</th>
<th>mIoU%</th>
</tr>
</thead>
<tbody>
<tr>
<td>SegNet [4]</td>
<td>59.10</td>
</tr>
<tr>
<td>LRR [21]</td>
<td>79.30</td>
</tr>
<tr>
<td>Dilation-8 [63]</td>
<td>75.30</td>
</tr>
<tr>
<td>FCN-8s [38]</td>
<td>67.20</td>
</tr>
<tr>
<td>ESPNet [40]</td>
<td>63.01</td>
</tr>
<tr>
<td>DeepLab [9]</td>
<td>65.3</td>
</tr>
<tr>
<td>RefineNet [34]</td>
<td>79.70</td>
</tr>
<tr>
<td>PSPNet [65]</td>
<td>82.40</td>
</tr>
<tr>
<td>DeepLabV3+ [112]</td>
<td>87.80</td>
</tr>
<tr>
<td>Ours</td>
<td>63.5</td>
</tr>
</tbody>
</table>

6.3. Results on PASCAL VOC 2012

We tested our network architecture against the popular PASCAL VOC 2012 segmentation dataset [20] which contains 20 object categories and a background class. It is composed of 1464, 1448 and 1456 images for the training, validation and test sets respectively. Following [38, 10, 42, 16] we used additional images to train our network with data annotation of [23] resulting in 10582 1449 and 1456 images for training, validation and testing. Table 9 shows a comparison of our method with state-of-the-art efficient architectures. Our architecture yields very good results with respect to other efficiency-oriented architectures and even outperforms some accuracy-oriented methods.

6.4. Speed

We report in Table 10 the speed of our network on a high-end Titan Xp GPU with Pytorch 1.0 and CUDA 10.0. Then we tested our architecture on an edge device: Nvidia Tegra TX1 (Jetson) with Pytorch 0.4.

7. Concluding Remarks

We presented a novel architecture for efficient scene understanding which includes a novel module to speed up the decoder part of encoder-decoder architectures and a module for Instance Segmentation based on iterative sampling. We tested our architecture on three different datasets showing that our network is fast and accurate compared to state-of-the-art efficient architectures.

Acknowledgments This work was supported by TEINVEIN, CUP: E96D17000110009 - Call "Accordi per la Ricerca e l’Innovazione", cofunded by POR FESR 2014-2020. We gratefully acknowledge the support of NVIDIA Corporation with the donation of a Titan Xp GPU used for this research.
References


