



# **Network Adjustment: Channel Search Guided by FLOPs Utilization Ratio**

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#### **Abstract**

Automatic designing computationally efficient neural networks has received much attention in recent years. Existing approaches either utilize network pruning or leverage the network architecture search methods. This paper presents a new framework named network adjustment, which considers network accuracy as a function of FLOPs, so that under each network configuration, one can estimate the **FLOPs utilization ratio** (FUR) for each layer and use it to determine whether to increase or decrease the number of channels on the layer. Note that FUR, like the gradient of a non-linear function, is accurate only in a small neighborhood of the current network. Hence, we design an iterative mechanism so that the initial network undergoes a number of steps, each of which has a small 'adjusting rate' to control the changes to the network. The computational overhead of the entire search process is reasonable, i.e., comparable to that of re-training the final model from scratch. Experiments on standard image classification datasets and a wide range of base networks demonstrate the effectiveness of our approach, which consistently outperforms the pruning counterpart. The code is available at https: //github.com/danczs/NetworkAdjustment.

### 1. Introduction

In the deep learning era [24], as researchers designed more and more complicated neural networks [23, 35, 14, 19, 50] for various computer vision problems [8, 30], it becomes increasingly important to find efficient architectures that are friendly to real-world applications. The computational overhead of a deep neural network is closely related to its width, *i.e.*, the number of channels on each layer of it. Beyond manually designed networks which have fixed configurations of width [14, 35, 38], researchers have also explored the direction of automatically determining the numbers of channels. Representative methods include network pruning [25, 28, 21] and channel search [40, 1], both of which obtained success in improving the computational

efficiency (accuracy with respect to overhead) of the network. However, both of these two approaches have some limitations. Network pruning methods usually start with a wide network and try to reduce the channel number of each layer. These methods are mostly not sensitive to specific efficiency criteria (*e.g.*, FLOPs) so that the computational resource is not always assigned to the places that need it most. On the other hand, the main drawbacks of the network search techniques are the computational complexity and search strategy which limit the search space and increase the search cost.

In this paper, we propose a novel pipeline named **net**work adjustment which starts with a pre-trained network, and simultaneously adds channels to some layers and subtracts channels from some others, with the criterion of the utilization ratio of resources at each layer. Network adjustment stands out from existing approaches in two aspects. **First**, we can estimate the state of each individual convolutional layer and optimize the channel numbers simultaneously. Therefore, our method is very efficient for channel search. **Second**, we search the channel configuration with an efficiency-aware strategy, by which we can find computationally efficient networks. The top-level design of our approach is an iterative process, in which each iteration involves training the given network, computing the utilization ratio on each layer, and adjusting the channel numbers accordingly. The computational overhead of the entire process is often comparable to that of training the initial network from scratch, yet we can observe gradual improvements of computational efficiency throughout iterations.

As a particular example, we aim to optimize the FLOPs (floating point operations) of deep networks, and calculate the **FLOPs utilization ratio** (FUR) for this purpose. FUR is defined on **each layer** of a network, measuring the accuracy gain that can be obtained by increasing unit FLOPs on that layer. To compute it, we assume that network accuracy is a function of FLOPs and that FUR, like the gradient of a non-linear function, can be estimated with an acceptable error in a small neighborhood of each state. To mimic a small amount of FLOPs change, we propose a randomized

approach which discards channels with a small drop probability (also named as SpatialDropout [41]) in each layer individually, and approximate FUR to be the ratio of accuracy drop over FLOPs drop, assuming that SpatialDropout removes the equivalent number (may be a floating point number) of channels. Note that FUR computation only involves network inference and thus executes efficiently.

We evaluate network adjustment on the standard benchmark of designing efficient networks. On both the CIFAR-100 and ImageNet datasets, our approach consistently improves the accuracy of a few popular network backbones, with the FLOPs barely changed. Compared with state-of-the-art structured pruning or channel search algorithms, network adjustment also enjoys advantages in terms of recognition accuracy under the same FLOPs.

The remainder of this paper is organized as follows. Section 2 briefly reviews the previous literature, and Section 3 elaborates the network adjustment approach. Experiments are shown in Section 4 and conclusions are drawn in Section 5.

#### 2. Related Work

The number of channels in each layer is an important factor in designing deep neural networks. Researchers started with empirical methods. A popular example is VG-GNet [35] which increases the channel number each time when the spatial resolution is reduced. This strategy was followed by a lot of work with either manual [46, 50, 38, 37] or automatic architecture designs [50, 33, 27, 32]. There also exist methods that increased the channel number gradually with the layer index, such as PyramidNet [11] and DenseNet [19]. To reduce the computational burden, researchers proposed a 'bottleneck' module [14] which reduces the number of channels before a costly computation and recovers it thereafter. Nevertheless, such work requires considerable manual efforts and the designed architectures were believed imperfect in many aspects.

To explore a larger number of possibilities, the idea of neural architecture search (NAS) was introduced [50, 45], so that the number of channels at each layer is determined with an automatic algorithm guided by general heuristics, e.g., the computational efficiency of the network. A typical example is to optimize a RNN controller to control the strategy of sampling architectures from the large search space [40, 18]. However, the computational burden largely limits the search space of such methods. For acceleration, researchers had to reduce the number of candidates to be used in each layer [1], or trained a wide network beforehand and sampled channels from each layer to mimic the behavior of networks with different widths [10, 3, 36]. Both strategies introduced inaccuracy to the network, which leads to sub-optimal search results. The differentiable methods [27, 2, 47] have not been applied to channel search yet.

Another line of research lies in channel pruning, which aims to speed up the network by removing a part of channels which contribute little to recognition [25, 28, 21]. The typical pipeline of pruning starts with a well-trained network, on which various criteria are used to measure the importance of a neuron [13, 12], a channel [25, 28], or a layer [44, 43]. After that, less important units are removed and the network gets fine-tuned [25]. Recently, researchers started rethinking the value or working mechanism of network pruning [29, 7], which led to novel designs of pruning approaches [48, 31].

# 3. Our Approach

#### 3.1. Problem

Given a network  $\mathbb{N}$  with L convolutional layers, a channel configuration of  $\mathbf{c} = (c_1, c_2, ..., c_L)$ , and a FLOPs function, FLOPs( $\mathbf{c}$ ), the goal is to improve the network performance by adjusting the channel configuration with predefined FLOPs,  $F_0$ :

$$\mathbf{c}^* = \arg\max_{\mathbf{c}} \mathrm{Acc}_{\mathrm{val}}(\mathbf{c}, \mathbf{W}^*)$$
 (1)

s.t. 
$$FLOPs(\mathbf{c}) = F_0$$
,  $\mathbf{W}^* = \arg \max_{\mathbf{W}} Acc_{train}(\mathbf{c}, \mathbf{W})$ 

where **W** is the parameters of the network and  $F_0$  is the FLOPs of the initial network. Acc<sub>train</sub> and Acc<sub>val</sub> denote the training and validation accuracy, respectively.

The problem is the channel numbers  ${\bf c}$  are discrete integers for a network and cannot be solved by optimization algorithms directly. To solve  ${\bf c}$  like gradient based optimization algorithms, we, therefore, propose a method to estimate the gradients of them, which will be detailed in Section 3.3. The estimated gradients are also named as FLOPs utilization ratios or FLOPs utilization efficiency in this paper. The gradients of  ${\bf c}$  are positive in general because most networks will benefit from increasing channels. To ensure FLOPs( ${\bf c}$ ) =  $F_0$ , we increase the channels in the layers with higher FURs and reduce the same amount of FLOPs from the layers with lower FURs. The channel adjustment will be executed iteratively like gradient based optimization algorithms until the FURs are similar in every layer.

#### 3.2. Pipeline: Network Adjustment

Our method aims to search for more efficient channel configuration by considering the efficiency more accurately. During search, the channel numbers are adjusted iteratively. There are three steps within each iteration: network training, FUR estimation and channel adjustment as shown in Figure 1. **First**, we optimize the current network on the training set. This process starts from scratch, but the number of epochs can be significantly smaller than a regular, complete training process because we only need to determine the FLOPs utilization efficiency of each layer, which

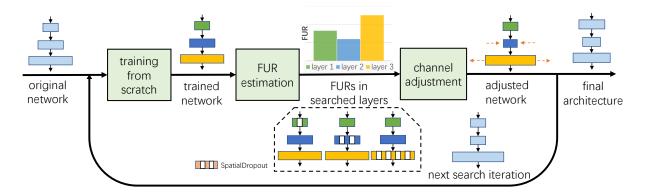


Figure 1. The framework of our channel number search approach (best viewed in color). The leftmost shows the original network, which undergoes an iterative process. In each round, the current network is trained from scratch, after which we estimate the FUR of each layer (marked in different colors) by individually performing SpatialDropout [41] on each layer. A taller bar indicates a higher FUR. After that, we choose the layers (only 1 in this example) with the highest FUR and increase its width, and do the opposite thing for the layers with the lowest FUR. This iteration continues till convergence, or a pre-defined number of iterations has been arrived.

does not necessarily mean that the network has arrived at a high accuracy. **Second**, we compute the FLOPs utilization ratio on each layer with the method described in the next part. This process only involves sampling-based inference on the validation set, and is thus relatively fast. **Third** and last, we use a pre-defined adjusting rate (similar to learning rate) to increase the numbers of channels of a few layers with top-ranked FURs, and decrease the numbers of a few layers with bottom-ranked FURs. The FLOPs of the entire network must remain close to the original amount after each iteration, if not, we use a scale function to slightly increase or decrease the channel numbers of all layers [9]. A typical search process involves a few (*e.g.*, 10) iterations, or it can be terminated if the accuracy is saturated on the validation set.

The pseudo code is summarized in Algorithm 1. The validation set is usually split from the original training set. The function 'Scale' scales the channel numbers to keep the FLOPs unchanged after adjustment. After search, the network with configuration  $\mathbf{c}^{\star}$  will be trained on the full training set and evaluated on the testing set.

### Difference from Prior Work

Before continuing with the technical part, we elaborate the difference between our approach and two families of prior work. The difference between our method and **network pruning** mainly lies in two aspects. First, pruning methods mostly focus on finding the less important channels and evaluate it with absolute performance drop [48] or reconstruction loss [21]. In contrast, our method considers the channels in a layer as a whole and evaluates the layer with FUR. Second, our method adjusts the channel configuration finely and iteratively while most pruning methods obtain the pruned model from a pre-trained wide model di-

rectly.

Our method is also different from **channel search** methods. Although many NAS systems update the channel numbers iteratively [50, 40], their methods usually need hundreds of GPU days to converge. That is because their search systems can only update channel numbers according to the network performance. In our method, we evaluate the FUR in each layer to guide the search process and the search system can converge after a few iterations (*e.g.*, 10). Additionally, limited by memory and computational resource, most NAS systems only search configuration from a few channel number candidates. Our method, however, can learn the channel configuration with an increased degree of freedom.

#### 3.3. FLOPs Utilization Ratio

Following the above, the **FLOPs utilization ratio** (FUR) of a layer is defined to be the contribution that a unit amount of FLOPs makes to the network accuracy, i.e.,  $\partial Acc_{val}(\mathbf{c}, \mathbf{W}^*)/\partial FLOPs(\mathbf{c})$ . To compute this quantity, we make use of the channel numbers, c, as an intermediate variable, and compute two quantities. The first quantity is the gradient of  $Acc_{val}(\mathbf{c}, \mathbf{W}^*)$  with respect to  $\mathbf{c}$ , denoted as  $\partial \mathrm{Acc}_{\mathrm{val}}(\mathbf{c}, \mathbf{W}^{\star})/\partial \mathbf{c}$ . In particular, an element in this vector,  $\partial \text{Acc}_{\text{val}}(\mathbf{c}, \mathbf{W}^{\star})/\partial c_l$ , indicates the contribution that a unit channel makes to the final accuracy. The second quantity involves FLOPs(c), and we use another vector, f, which shares the same dimensionality with c and each element,  $f_l$ , denotes the FLOPs in the l-th layer. We have FLOPs(c) =  $\sum_{l=1}^{L} f_l + \text{const}$ , where the constant is often determined by the architecture topology. For a convolutional layer,  $f_l$  is proportional to the spatial resolution, input and output channel numbers, and the convolutional kernel size of the l-th layer. Similarly, computing  $\partial FLOPs(\mathbf{c})/\partial c_l$ obtains the contribution that a unit channel makes to the re-

# Algorithm 1 Network Adjustment

**Input:** training set  $\mathcal{D}_{\text{train}}$ , validation set  $\mathcal{D}_{\text{val}}$ , initial channel configuration  $\mathbf{c}^{(1)}$ , adjusted layers K, adjusting rate  $r_A$ , max iterations T;

**Output:** adjusted channel configuration  $c^*$ ;

```
1: for t = 1, 2, ..., T do
  2:
               Build a network \mathbb{N} with \mathbf{c}^{(t)};
               Train the network \mathbb{N} on the training set \mathcal{D}_{train};
  3:
              Test the network \mathbb{N} and obtain the accuracy \mathrm{Acc}_{\mathrm{val}}^{(t)}
  4:
               on the validation set \mathcal{D}_{\text{val}};
               Compute the FUR for each layer of N on the valida-
              tion set: \mathbf{F}^{(t)} = (\mathrm{FUR}_1^{(t)}, \mathrm{FUR}_2^{(t)}, ..., \mathrm{FUR}_L^{(t)});
Sort the elements in \mathbf{F}^{(t)} and obtain a top index set,
              \mathcal{I}_{\text{top}}^{(t)}, with K largest elements in \mathbf{F}^{(t)}, and a bottom
             set \mathcal{I}_{\mathrm{bot}}^{(t)}, with K smallest elements in \mathbf{F}^{(t)}; \mathbf{c}^{t+1} = \mathbf{c}^t; for i in \mathcal{I}_{\mathrm{top}}^{(t)} do c_i^{(t+1)} = \mathrm{Round}(c_i^{(t)} + r_{\mathrm{A}} \cdot c_i^{(1)});
  7:
  8:
  9:
             end for for i in \mathcal{I}_{\mathrm{bot}}^{(t)} do c_i^{(t+1)} = \mathrm{Round}(c_i^{(t)} - r_{\mathrm{A}} \cdot c_i^{(1)});
 10:
 11:
 12:
 13:
              \mathbf{c}^{(t+1)} = \text{Scale}(\mathbf{c}^{(t+1)});
16: t^{\star} = \arg \max_{t} (\mathbf{Acc}_{\mathrm{val}}^{(t)});
17: \mathbf{c}^{\star} = \mathbf{c}^{(t^{\star})};
```

source usage. With these quantities, the FUR of the l-th layer is defined as:

Return: c\*.

$$FUR_{l} = \frac{\partial Acc_{val}(\mathbf{c}, \mathbf{W}^{*})}{\partial FLOPs(\mathbf{c})} = \frac{\partial Acc_{val}(\mathbf{c}, \mathbf{W}^{*})/\partial c_{l}}{\partial FLOPs(\mathbf{c})/\partial c_{l}}$$
(2)

Here we have omitted the notation of the network topology,  $\mathbb{N}$ , in all terms, for simplicity.

In writing  $\partial \mathrm{Acc_{val}}(\mathbf{c}, \mathbf{W}^{\star})/\partial c_l$  and  $\partial \mathrm{FLOPs}(\mathbf{c})/\partial c_l$ , we assume that  $c_l$ , the number of channels in each layer, can take continuous (non-integer) values. To mimic this behavior, we introduce a probabilistic mechanism which involves adding SpatialDropout [41] to each layer individually, and observing how the network accuracy is affected by this operation. SpatialDropout is proposed as a regularization method and randomly discards channels with a certain probability. To our best knowledge, it is the first time that SpatialDropout is used for channel evaluation.

To reduce the systematic error in computing FUR, we discard the same FLOPs in every layer. In other words, the change of FLOPs is the same for all layers, by which we can rank the FURs with the accuracy drop directly. Since the data drop probability is continuous, we can simulate

dropping fractional channels to guarantee that the discarded FLOPs in all layers are the same.

### 3.4. Towards a Slightly Different Network

FUR is like the gradient of network performance with respect to FLOPs and is very likely to be accurate only for the current network. Therefore, like the gradient based optimization method, we only slightly adjust the channel numbers in each iteration, so that FURs are still accurate enough.

Specifically, we increase the channels of the top-k layers ranked by FURs and reduce the channels in the bottom-k layers. The hyperparameter k will be deceased during search. The 'adjusting rate' is like the learning rate in gradient based optimization algorithms and is used to adjust the channels in an iteration. It should not be too big (We use 0.1 for most of our experiments). Big adjusting rates may make the search system unstable.

When the channel configuration is updated, the FUR in each layer will change. To obtain new FUR of the new network, we will retrain the new networks from scratch. Since evaluating a trained network on the validation set is relatively fast, the most time-consuming step in our framework is training a network. In every searching iteration, a network should be trained. For tasks like training big models on ImageNet, this strategy becomes impractical. There are two methods that can tackle this problem: dataset sampling [45] and reducing the training epochs [40]. In our experiment, it is observed that reducing the training epochs has less effect on FUR estimation than dataset sampling. When training for much fewer epochs, the FUR ranking can still be revealed, even though the network performance is relatively low. Hence, we train our model on ImageNet for about ten percent of the full epochs. In this way, the time for search channel numbers on ImageNet is comparable to training a model on ImageNet.

### 3.5. Implementation Details

**SpatialDropout and network training.** As mentioned above, we evaluate the FUR by randomly discarding some channels (SpatialDropout). To make the evaluation reasonable, we should reduce unexpected changes (*e.g.* the change of data distribution) caused by discarding channels as much as possible, so that the test accuracy can reveal the real effect caused by channel change. Generally speaking, discarding some channels will not sharply degrade the network performance. The network accuracy usually decreases smoothly as the drop probability increases. In fact, even discarding whole layers in residual blocks will not disable a residual network [20, 42].

Moreover, we use some tricks to make the neural layers accustomed to data drop. Firstly, Spatial Dropout is used during network training. Since the network is trained with

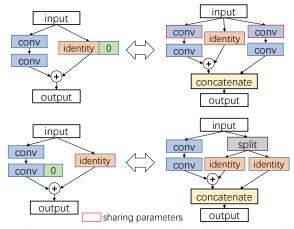


Figure 2. The equivalent structure when utilizing zero padding. Padding zeros to the path with fewer channels is equivalent to introducing a new path to the residual block.

SpatialDropout, it will not be sensitive to SpatialDropout during test. Secondly, the data distribution is balanced after SpatialDropout like standard Dropout. For example, if 10% of the channels are discarded, the remaining channels will be scaled by 10/9. Finally, we reduce the disharmony between BN and data drop. There exists disharmony between BN and dropout [26]. It is demonstrated that Gaussian dropout and using dropout after BN can solve this problem [26].

**Zero padding for neural layers.** For single-path networks like VGGNets, the channel numbers in every layer can be set freely. For multi-path networks like ResNets, there are some constraints on setting channel numbers. Taking residual blocks as an example, since the output of convolutions should be added to the residual shortcut, the channel numbers of these two paths should be the same. To solve this problem, we simply pad zeros to the path with fewer channels. Zero padding not only enables us to set the channel numbers freely in multi-path networks but also introduces some network structure implicitly. As displayed in Figure 2, in residual blocks, padding zeros to shortcut is equivalent to add a convolution path between input and output [11]. Padding zeros to convolution output is equivalent to concatenate an extra shortcut to the output. Therefore, searching channel numbers for multi-path networks with zero padding is also searching the network structure implicitly.

### 4. Experiments

#### 4.1. Results on CIFAR-100

We test our method on CIFAR-100, since the results on CIFAR-100 are more stable than those on CIFAR-10. CIFAR-100 is a publicly available dataset proposed by [22]. The dataset consists of  $32\times32$  colored natural im-

Table 1. Accuracy (%) comparison on CIFAR-100 between our approach and other competitors. Here, 'D' indicates the network depth, 'FT' indicates that the searched network is only fine-tuned, not trained from scratch, and 'KD' is for knowledge distillation. The mark 'imp' indicates our own implementation. Legend: LCCL [5], SFP [15], FPGM [16], TAS [6].

D	Method	FT	KD	Acc (%)	FLOPs (M)
20 -	LCCL			64.66 (-2.87)	27.3
	SFP			64.37 (-3.25)	24.3
	FPGM			66.86 (-0.76)	24.3
	TAS		$\checkmark$	68.90 (+0.21)	22.4
20 -	original (imp)			69.52	40.1
	original 0.75×			65.90	22.7
	searched $0.75 \times$			69.04 (-0.48)	22.4
	searched $0.75 \times$		$\checkmark$	70.03 (+0.51)	22.4
	LCCL			67.39 (-2.69)	43.2
	SFP			68.37 (-1.40)	40.3
	FPGM			68.52 (-1.25)	40.3
-	TAS			68.95 (-1.66)	42.5
32	TAS	$\checkmark$		69.70 (-0.91)	42.5
32	TAS		$\checkmark$	72.41 (+1.80)	42.5
	original (imp)			72.57	68.4
	original 0.75×			69.68	38.5
	searched $0.75 \times$			71.93 (-0.64)	42.2
	searched $0.75 \times$		$\checkmark$	73.82 (+1.25)	42.2
	LCCL			70.78 (-2.01)	173
	SFP	$\checkmark$		71.28 (-2.86)	121
	FPGM	$\checkmark$		72.55 (-1.59)	121
110_	TAS		$\checkmark$	73.16 (-1.90)	120
	original (imp)			75.68	252
	original $0.7 \times$			73.46	119
	searched $0.7 \times$			74.84 (-0.84)	120
	searched 0.7×		✓	75.96 (+0.28)	120

ages that fall in 100 classes. There are 50,000 images in the training set and 10,000 images in the testing set. We randomly sample 5,000 images from the training set as the validation set to search the channel numbers. Afterwards, we retrain the searched networks on the whole training set and release the results on the testing set. Data augmentation is conducted during training following the common practice [14, 49]. During search and test, the networks are trained for 200 epochs and SpatialDropout is used for convolutional layers. The learning rate is dropped from 0.15 to 1e-3 with cosine annealing.

# Comparison to Network Pruning

The results of our method and some pruning methods are shown in Table 1. To compare our method to the pruning methods, we first narrow down the original networks to a comparable FLOPs level and then search for channel configuration based on the thin networks.

For network testing, the pruning models are usually

Table 2. The accuracy (on CIFAR-100) and FLOPs of ResNet-20, throughout a complete iterative process of network adjustment.

iteration	0 (original network)	1	2	3	4	5	6	7	8	9	10
accuracy (%)	69.52	70.12	70.24	70.34	70.48	70.94	71.09	71.55	71.47	71.36	71.57
FLOPs (M)	40.1	40.5	39.8	40.2	40.1	40.3	40.4	40.3	40.3	40.4	40.2

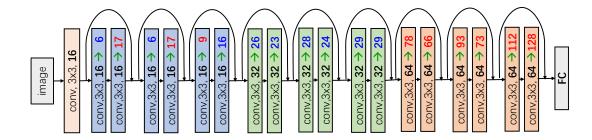


Figure 3. The configuration of ResNet-20 before and after the entire network adjustment process (10 rounds) on CIFAR-100. Red and blue indicate increased and decreased channel numbers, respectively.

initialized with the weights of the original model (finetuning), which is usually better than training from scratch. Some methods transfer the knowledge in original models by knowledge distillation [17], which usually can significantly improve the pruned networks. Since we aim to search for better channel configuration, our models are mainly evaluated by training from scratch. We also report the knowledge distillation results for a fair comparison. The methods in Table 1 have different baselines. Thus the main criterion is the accuracy drops of the pruning networks compared to their original networks. As can be seen, our searched models outperform most of the pruning methods on accuracy drop. For ResNet-32, although the TAS model with knowledge distillation performs better than our model, our search network outperforms the TAS model when trained from scratch, which demonstrates that our channel configuration is better. For ResNet-110, our searched model that is trained from scratch even performs better than the TAS model with knowledge distillation.

### • Diagnostic Studies

Different iterations on CIFAR-100. The channel number search results of ResNet-20 on CIFAR-100 is shown in Table 2. As can be seen, the performance of the network is consistently improved in the first seven iterations. Afterwards, the performance gain becomes saturated. Since we have balanced the increase and decrease of FLOPs in different layers, the FLOPs of the network are almost unchanged. Note that the parameters of the networks are increased. It reveals that the network is exploring the unconstrained resources to improve its performance. A similar phenomenon is observed in [4]. We think the exchange of parameters and performance can be a new direction of designing networks. Because for most current devices, FLOPs rather than pa-

Table 3. The searched results for different networks.						
Networks	method	Accuray (%)	FLOPs (M)			
ResNet-20	original	69.52	40.1			
	searched	71.57 (+2.05)	40.2			
ResNet-32	original searched	72.57 74.14 (+1.57)	68.4 68.2			
ResNet-56	original	74.90	125			
	searched	76.15 (+1.25)	126			
ResNet-110	original	75.68	252			
	searched	76.98 (+1.30)	254			
ResNet-20 2.0×	original	75.60	160			
	searched	76.87 (+1.27)	161			
ResNet-20 4.0×	original searched	78.84 79.51 (+0.67)	642 641			
Plain-20	original	65.62	40.1			
	searched	68.89 (+3.27)	40.1			
DenseNet-40	original	76.32	282			
	searched	76.95 (+0.63)	282			
PyramidNet-20	original	66.58	40.4			
	searched	71.53 (+4.95)	40.1			

rameters are the bottleneck.

The searched architecture. The searched channel configuration in ResNet-20 is different from the original network both at network-level and substructure-level. The structure of the original ResNet-20 and searched ResNet-20 are shown in Figure 3. At network-level, more channels are assigned to the deep layers. It demonstrates that these layers are more efficient in FLOPs utilization. At substructure-level, as for the layers that closed to the in-

put, a more efficient residual block structure is found. In the first residual block, the output channel number of the first layer decreases from 16 to 6. Afterwards, the channel number is increased to 17 by the second convolutional layer. Although the output channel numbers of the block are slightly increased compared to the original block, the FLOPs of the residual block are sharply reduced. In this residual block, reducing the output of the first convolution will decrease the FLOPs of the two convolutional layers at the same time. Reducing the output of the second convolution may only decrease the FLOPs of the second layer, because the output will be added to the residual connection and the FLOPs of the following layer will not change if its channels are fewer than the residual connection. Our method is sensitive to FLOPs utilization and, thus, can find this 'compression-expansion' structure. The result also indicates that our method can not only reset the channel numbers at network-level but also find more efficient substructure in neural networks.

Without FUR. We study FUR by searching ResNet-20 without it. It means that when evaluating channels, we randomly drop a fixed ratio of channels in each layer and rank the layers by the accuracy drops instead of FURs. Under this setting, the searched ResNet-20, which achieves 71.06%, still outperforms the original network (69.52%), but performs worse than the network searched with FUR (71.57%). At substructure-level, the fixed-ratio network can not learn the 'compression-expansion' structure shown in Figure 3, because it is not aware of the difference of FLOPs utilization between the two convolutional layers.

Different initialization of channel numbers. To test the effect of the initialization channel numbers, we construct a 20 layer PyramidNet [11]. The channels of this network are linearly increased and the FLOPs are similar to ResNet-20. With the same FLOPs, ResNet-20 outperforms PyramidNet-20 by 2.94%. From the perspective of FLOPs, PyramidNet-20 is a bad initialization of channel configuration for a 20 layer residual network. After channel search, the two searched networks achieve similar performance as shown in Table 3. It indicates that our method can overcome the bad initialization of channel numbers.

**Different depths, different widths, different architectures.** We test our model on different width and depth ResNets. When increasing depth, our method still works well. ResNet-32, 56 and 110 outperform the original networks by over 1%. For these deep networks, more FLOPs are assigned to the last several layers like ResNet-20. In ResNet-56 and 110, the 'compression-expansion' structure is learned not only in the first stage but also in the last network stage. ResNet-20 2.0× and ResNet-20 4.0× are networks widened from ResNet-20 by 2.0 and 4.0. These wide networks can also benefit from channel adjustment.

Plain-20 is the non-residual version of ResNet-20. The

searched result on Plain-20 outperforms the original network by 3.27%. The searched channel configuration of DenseNet-40 is different from ResNet-20. More FLOPs are assigned to 28th-36th layers rather than the last several layers. It indicates that the FURs in these layers are higher than the others for the original DenseNet-40.

Channel configuration transfer. To evaluate the generalization of the searched channel configuration, the searched configuration of ResNet-20 is transferred to ResNet-20  $2.0\times$  by doubling the channels. The transferred model has the same FLOPs with ResNet-20  $2.0\times$  and the accuracy is 76.34% on CIFAR-100. Although the transferred model outperforms the original model, it performs worse than the directly searched model (76.87%). It indicates that there are biases when transferring channel configuration among networks with different widths.

### 4.2. Results on ImageNet

ILSVRC2012 is a subset of ImageNet database [34]. There are 1.3M, 50K and 150K images in training, validation and testing set. The images fall into 1K categories. We randomly sample 50K images from the training set as the searching validation set. Afterwards, we retrain the searched networks on the whole training set and release the results on the original validation set. The images are augmented with the method in [39]. On ImageNet, networks are trained for 10 epochs in each iteration and the channel number of the first convolutional layer is fixed during search. During the re-training process, ResNet-18 is trained on the full training set for 90 epochs. The learning rate is decayed from 0.1 to 0.001 with cosine annealing. MnasNet is trained for 500 epochs and the learning rate is dropped from 0.075 to 0.0001.

**First**, we compare our results to those produced by network pruning. For ResNet-18, the searched model outperforms the original model by 0.91% for top-1 accuracy as shown in Table 4. To compare our method to pruning approaches, we scale the channel numbers in ResNet-18 by a factor of 0.8 and search for channel configuration based on it. The top-1 accuracy of ResNet-18 0.8× outperforms the other pruning approaches on accuracy drop and with knowledge distillation, our model performs much better.

The original and searched ResNet-18 0.8× are shown in Figure 4. As can be seen, the first stage also learned the 'compression-expansion' structure and more FLOPs are assigned to the last several layers. In addition, the downsampling layers in the networks are assigned more channels, indicating that the downsampling layers are different from other layers and should be specifically designed. We also show the FURs of some layers during the adjustment process in Figure 5, from which one can observe how the FURs differ from each other in the beginning and gradually become similar during the adjustment.

Table 4. Top-1 accuracy (%) comparison on ImageNet between our approach and other competitors. Here, 'FT' indicates that the searched network is only fine-tuned, not trained from scratch, and 'KD' is for knowledge distillation. The mark 'imp' indicates our own implementation. Legend: LCCL [5], SFP [15], FPGM [16], TAS [6], AutoSlim [48].

Networks	Method	FT	KD	Top-1 (%)	Top-5 (%)	FLOPs (M)	Parameters(M)
	LCCL			66.33 (-3.65)	86.94 (-2.29)	1.19E3	-
	SFP			67.10 (-3.18)	87.78 (-1.85)	1.06E3	-
	FPGM	$\checkmark$		68.41 (-1.87)	88.48 (-1.15)	1.06E3	-
ResNet-18	TAS		$\checkmark$	69.15 (-1.5)	89.19 (-0.68)	1.21E3	-
Resnet-10	original (imp)			70.31	89.45	1.81E3	11.7
	searched			71.22 (+0.91)	90.07 (+0.62)	1.81E3	18.5
	searched $0.8 \times$			69.41 (-0.90)	88.71 (-0.74)	1.17E3	11.8
	searched $0.8 \times$		$\checkmark$	71.00 (+0.69)	90.05 (+0.60)	1.17E3	11.8
MnasNet	original (imp)			74.21	91.83	312	3.89
	AutoSlim			74.60 (+0.60)	-	315	6.00
	ours			74.88 (+0.67)	92.15 (+0.32)	312	4.96

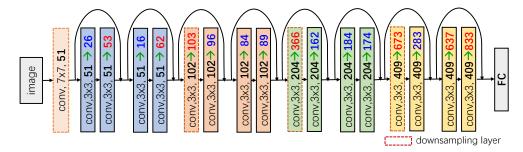


Figure 4. The configuration of ResNet-18  $0.8 \times$  before and after the entire network adjustment process (10 rounds) on ImageNet. Red and blue indicate increased and decreased channel numbers, respectively.

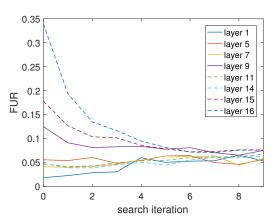


Figure 5. FURs of convolutional layers in ResNet-18  $0.8\times$ . We sampled 8 out of 16 layers for better visualization.

**Second**, we adjust beyond MnasNet [40], a searched network architecture based on MobileNetV2. By changing the channel configuration, we improve the network performance with the same FLOPs, as shown in Table 4. It indicates that our method can find the more efficient channel configuration compared to a NAS approach in a limited search space. Our method slightly outperforms AutoSlim with fewer parameters. Like ResNet-18 0.8×, the searched MnasNet also assigns more FLOPs to the downsampling

layers, which indicates that the current structure of downsampling can be further improved.

#### 5. Conclusions

This paper presents a new pipeline named network adjustment for designing efficient network architectures. Our approach is motivated by the idea of measuring how FLOPs are utilized in a pre-trained model, so that it is possible to adjust the number of network channels according to the efficiency of computational resource utilization. For this purpose, we consider network performance as a function of FLOPs and its 'gradient', FLOPs utilization ratio (FUR), can be estimated and utilized in a small neighborhood. Integrating all the above yields an iterative pipeline, which outperforms state-of-the-art network pruning methods in terms of recognition accuracy under the same amount of computation. Our research puts forward a new point that network accuracy and speed should be optimized jointly, for which network adjustment provides a preliminary solution. More efforts are required along this path.

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#### References

- [1] Han Cai, Ligeng Zhu, and Song Han. Proxylessnas: Direct neural architecture search on target task and hardware. *arXiv* preprint arXiv:1812.00332, 2018.
- [2] Xin Chen, Lingxi Xie, Jun Wu, and Qi Tian. Progressive differentiable architecture search: Bridging the depth gap between search and evaluation. arXiv preprint arXiv:1904.12760, 2019.
- [3] Yukang Chen, Tong Yang, Xiangyu Zhang, Gaofeng Meng, Chunhong Pan, and Jian Sun. Detnas: Neural architecture search on object detection. arXiv preprint arXiv:1903.10979, 2019.
- [4] Xiangxiang Chu, Bo Zhang, Ruijun Xu, and Jixiang Li. Fairnas: Rethinking evaluation fairness of weight sharing neural architecture search. arXiv preprint arXiv:1907.01845, 2019.
- [5] Xuanyi Dong, Junshi Huang, Yi Yang, and Shuicheng Yan. More is less: A more complicated network with less inference complexity. In *Proceedings of the IEEE Conference on Computer Vision and Pattern Recognition*, pages 5840–5848, 2017.
- [6] Xuanyi Dong and Yi Yang. Network pruning via transformable architecture search. *arXiv preprint arXiv:1905.09717*, 2019.
- [7] Jonathan Frankle and Michael Carbin. The lottery ticket hypothesis: Finding sparse, trainable neural networks. arXiv preprint arXiv:1803.03635, 2018.
- [8] Ross Girshick, Jeff Donahue, Trevor Darrell, and Jitendra Malik. Rich feature hierarchies for accurate object detection and semantic segmentation. In *Proceedings of the IEEE con*ference on computer vision and pattern recognition, pages 580–587, 2014.
- [9] Ariel Gordon, Elad Eban, Ofir Nachum, Bo Chen, Hao Wu, Tien-Ju Yang, and Edward Choi. Morphnet: Fast & simple resource-constrained structure learning of deep networks. In Proceedings of the IEEE Conference on Computer Vision and Pattern Recognition, pages 1586–1595, 2018.
- [10] Zichao Guo, Xiangyu Zhang, Haoyuan Mu, Wen Heng, Zechun Liu, Yichen Wei, and Jian Sun. Single path oneshot neural architecture search with uniform sampling. arXiv preprint arXiv:1904.00420, 2019.
- [11] Dongyoon Han, Jiwhan Kim, and Junmo Kim. Deep pyramidal residual networks. In *Proceedings of the IEEE Conference on Computer Vision and Pattern Recognition*, pages 5927–5935, 2017.
- [12] Song Han, Huizi Mao, and William J Dally. Deep compression: Compressing deep neural networks with pruning, trained quantization and huffman coding. arXiv preprint arXiv:1510.00149, 2015.
- [13] Song Han, Jeff Pool, John Tran, and William Dally. Learning both weights and connections for efficient neural network. In Advances in neural information processing systems, pages 1135–1143, 2015.
- [14] Kaiming He, Xiangyu Zhang, Shaoqing Ren, and Jian Sun. Deep residual learning for image recognition. In *Proceedings of the IEEE Conference on Computer Vision and Pattern Recognition*, pages 770–778, 2016.

- [15] Yang He, Guoliang Kang, Xuanyi Dong, Yanwei Fu, and Yi Yang. Soft filter pruning for accelerating deep convolutional neural networks. *arXiv* preprint arXiv:1808.06866, 2018.
- [16] Yang He, Ping Liu, Ziwei Wang, Zhilan Hu, and Yi Yang. Filter pruning via geometric median for deep convolutional neural networks acceleration. In *Proceedings of the IEEE Conference on Computer Vision and Pattern Recognition*, pages 4340–4349, 2019.
- [17] Geoffrey Hinton, Oriol Vinyals, and Jeff Dean. Distilling the knowledge in a neural network. *arXiv preprint arXiv:1503.02531*, 2015.
- [18] Andrew Howard, Mark Sandler, Grace Chu, Liang-Chieh Chen, Bo Chen, Mingxing Tan, Weijun Wang, Yukun Zhu, Ruoming Pang, Vijay Vasudevan, et al. Searching for mobilenetv3. arXiv preprint arXiv:1905.02244, 2019.
- [19] Gao Huang, Zhuang Liu, Laurens Van Der Maaten, and Kilian Q Weinberger. Densely connected convolutional networks. In CVPR, volume 1, page 3, 2017.
- [20] Gao Huang, Yu Sun, Zhuang Liu, Daniel Sedra, and Kilian Q Weinberger. Deep networks with stochastic depth. In European Conference on Computer Vision, pages 646–661. Springer, 2016.
- [21] Zehao Huang and Naiyan Wang. Data-driven sparse structure selection for deep neural networks. In *Proceedings of the European Conference on Computer Vision (ECCV)*, pages 304–320, 2018.
- [22] Alex Krizhevsky and Geoffrey Hinton. Learning multiple layers of features from tiny images. 2009.
- [23] Alex Krizhevsky, Ilya Sutskever, and Geoffrey E Hinton. Imagenet classification with deep convolutional neural networks. In Advances in neural information processing systems, pages 1097–1105, 2012.
- [24] Yann LeCun, Yoshua Bengio, and Geoffrey Hinton. Deep learning. *nature*, 521(7553):436–444, 2015.
- [25] Hao Li, Asim Kadav, Igor Durdanovic, Hanan Samet, and Hans Peter Graf. Pruning filters for efficient convnets. arXiv preprint arXiv:1608.08710, 2016.
- [26] Xiang Li, Shuo Chen, Xiaolin Hu, and Jian Yang. Understanding the disharmony between dropout and batch normalization by variance shift. In *Proceedings of the IEEE Con*ference on Computer Vision and Pattern Recognition, pages 2682–2690, 2019.
- [27] Hanxiao Liu, Karen Simonyan, and Yiming Yang. Darts: Differentiable architecture search. arXiv preprint arXiv:1806.09055, 2018.
- [28] Zhuang Liu, Jianguo Li, Zhiqiang Shen, Gao Huang, Shoumeng Yan, and Changshui Zhang. Learning efficient convolutional networks through network slimming. In Proceedings of the IEEE International Conference on Computer Vision, pages 2736–2744, 2017.
- [29] Zhuang Liu, Mingjie Sun, Tinghui Zhou, Gao Huang, and Trevor Darrell. Rethinking the value of network pruning. arXiv preprint arXiv:1810.05270, 2018.
- [30] Jonathan Long, Evan Shelhamer, and Trevor Darrell. Fully convolutional networks for semantic segmentation. In Proceedings of the IEEE conference on computer vision and pattern recognition, pages 3431–3440, 2015.

- [31] Sangkug Lym, Esha Choukse, Siavash Zangeneh, Wei Wen, Mattan Erez, and Sujay Shanghavi. Prunetrain: Gradual structured pruning from scratch for faster neural network training. *arXiv preprint arXiv:1901.09290*, 2019.
- [32] Hieu Pham, Melody Y Guan, Barret Zoph, Quoc V Le, and Jeff Dean. Efficient neural architecture search via parameter sharing. arXiv preprint arXiv:1802.03268, 2018.
- [33] Esteban Real, Alok Aggarwal, Yanping Huang, and Quoc V Le. Regularized evolution for image classifier architecture search. In *Proceedings of the AAAI Conference on Artificial Intelligence*, volume 33, pages 4780–4789, 2019.
- [34] Olga Russakovsky, Jia Deng, Hao Su, Jonathan Krause, Sanjeev Satheesh, Sean Ma, Zhiheng Huang, Andrej Karpathy, Aditya Khosla, Michael Bernstein, et al. Imagenet large scale visual recognition challenge. *International Journal of Computer Vision*, 115(3):211–252, 2015.
- [35] Karen Simonyan and Andrew Zisserman. Very deep convolutional networks for large-scale image recognition. *arXiv* preprint arXiv:1409.1556, 2014.
- [36] Dimitrios Stamoulis, Ruizhou Ding, Di Wang, Dimitrios Lymberopoulos, Bodhi Priyantha, Jie Liu, and Diana Marculescu. Single-path nas: Designing hardware-efficient convnets in less than 4 hours. arXiv preprint arXiv:1904.02877, 2019.
- [37] Christian Szegedy, Sergey Ioffe, Vincent Vanhoucke, and Alex Alemi. Inception-v4, inception-resnet and the impact of residual connections on learning. *arXiv preprint arXiv:1602.07261*, 2016.
- [38] Christian Szegedy, Wei Liu, Yangqing Jia, Pierre Sermanet, Scott Reed, Dragomir Anguelov, Dumitru Erhan, Vincent Vanhoucke, and Andrew Rabinovich. Going deeper with convolutions. In Proceedings of the IEEE Conference on Computer Vision and Pattern Recognition, pages 1–9, 2015.
- [39] Christian Szegedy, Vincent Vanhoucke, Sergey Ioffe, Jon Shlens, and Zbigniew Wojna. Rethinking the inception architecture for computer vision. In *Proceedings of the IEEE Conference on Computer Vision and Pattern Recognition*, pages 2818–2826, 2016.
- [40] Mingxing Tan, Bo Chen, Ruoming Pang, Vijay Vasudevan, Mark Sandler, Andrew Howard, and Quoc V Le. Mnasnet: Platform-aware neural architecture search for mobile. In Proceedings of the IEEE Conference on Computer Vision and Pattern Recognition, pages 2820–2828, 2019.
- [41] Jonathan Tompson, Ross Goroshin, Arjun Jain, Yann LeCun, and Christoph Bregler. Efficient object localization using convolutional networks. In *Proceedings of the IEEE Con*ference on Computer Vision and Pattern Recognition, pages 648–656, 2015.
- [42] Andreas Veit, Michael J Wilber, and Serge Belongie. Residual networks behave like ensembles of relatively shallow networks. In *Advances in Neural Information Processing Systems*, pages 550–558, 2016.
- [43] Xin Wang, Fisher Yu, Zi-Yi Dou, Trevor Darrell, and Joseph E Gonzalez. Skipnet: Learning dynamic routing in convolutional networks. In *Proceedings of the European Conference on Computer Vision (ECCV)*, pages 409–424, 2018.

- [44] Wei Wen, Chunpeng Wu, Yandan Wang, Yiran Chen, and Hai Li. Learning structured sparsity in deep neural networks. In Advances in neural information processing systems, pages 2074–2082, 2016.
- [45] Bichen Wu, Xiaoliang Dai, Peizhao Zhang, Yanghan Wang, Fei Sun, Yiming Wu, Yuandong Tian, Peter Vajda, Yangqing Jia, and Kurt Keutzer. Fbnet: Hardware-aware efficient convnet design via differentiable neural architecture search. In Proceedings of the IEEE Conference on Computer Vision and Pattern Recognition, pages 10734–10742, 2019.
- [46] Saining Xie, Ross Girshick, Piotr Dollár, Zhuowen Tu, and Kaiming He. Aggregated residual transformations for deep neural networks. arXiv preprint arXiv:1611.05431, 2016.
- [47] Yuhui Xu, Lingxi Xie, Xiaopeng Zhang, Xin Chen, Guo-Jun Qi, Qi Tian, and Hongkai Xiong. Pc-darts: Partial channel connections for memory-efficient differentiable architecture search. *arXiv preprint arXiv:1907.05737*, 2019.
- [48] Jiahui Yu and Thomas Huang. Network slimming by slimmable networks: Towards one-shot architecture search for channel numbers. arXiv preprint arXiv:1903.11728, 2019.
- [49] Sergey Zagoruyko and Nikos Komodakis. Wide residual networks. arXiv preprint arXiv:1605.07146, 2016.
- [50] Barret Zoph, Vijay Vasudevan, Jonathon Shlens, and Quoc V Le. Learning transferable architectures for scalable image recognition. arXiv preprint arXiv:1707.07012, 2(6), 2017.