A. Implementation Details

Initialization of Video Encoders. All the video encoders are initialized with the pretrained models on Kinetics-400 [2]. This is similar to the common practice of ImageNet [3] pre-training in image understanding.

Combination with External Video Labels on ActivityNet. As mentioned in the main body of the paper, most videos on ActivityNet only contain one action class. Therefore, most previous works [1, 9, 10, 15, 17, 22–24, 26, 28], including some end-to-end methods [7, 11, 16], decompose temporal action detection (TAD) into class-agnostic temporal localization and video-level action classification. Following these works, we use the video-level action classification results of [27], a winning solution in ActivityNet Challenge 2017. To be concrete, we assign the top two video-level classes predicted by [27] to all class-agnostic detections, forming class-aware detections. The confidence score of the original class-agnostic detection and the classification score by [27] are fused by multiplication.

Implementation Details of AFSD. When implementing AFSD [7], we follow the details in their official code. They use a smaller batch size (1 vs. 4), a smaller learning rate (10^{-5} vs. 10^{-4}), and a longer training schedule (16 epochs vs. 12 epochs). We tried the setting defined in this paper but observed a performance drop of around 1.5% average mAP. Therefore, we stick to the original settings.

B. Additional Results

The Effect of Batch Size. Tab. A1 studies the effect of batch size for training. We change the learning rate following the linear scaling rule [5] when changing the batch size. We observe that increasing batch size from 4 (the default setting) to 16 results in similar performance. Therefore, we may use a larger batch size to improve GPU utilization and speed up training. For example, we can finish training of TadTR [14] with TSM ResNet-18 [8] encoder on ActivityNet using two NVIDIA RTX 3090 GPUs in 41 minutes.

Table A1. The effect of batch size, measured by average mAP on ActivityNet. Encoder: TSM ResNet-18. Detector: TadTR. Only cropping and horizontal flipping augmentations are used. All models are trained using a single GPU (except * uses two GPUs).

<table>
<thead>
<tr>
<th>Batch Size</th>
<th>4</th>
<th>8</th>
<th>16</th>
</tr>
</thead>
<tbody>
<tr>
<td>mAP</td>
<td>33.40</td>
<td>33.43</td>
<td>33.25</td>
</tr>
<tr>
<td>Training Time</td>
<td>96min</td>
<td>85min</td>
<td>41min*</td>
</tr>
</tbody>
</table>

Even so, we stick to a relatively small batch size so that our experiments can be reproduced more easily.

Generality of the Effectiveness of End-to-End Training. In the main body of this paper, we validate the effectiveness of end-to-end training on TadTR with TSM [8] encoders. Here we also conduct the validation on more video encoders (SlowFast [4] and I3D [2]) and detection heads (AFSD [7] and G-TAD [23]). The results are summarized in Tab. A2. All results are obtained with the default setting. We see that end-to-end learning consistently improves detection performance. The performance gain ranges between 9.79% and 10.54% on THUMOS14 [6]. On ActivityNet, the performance gain is at least 1.77%. The results show that the effectiveness of end-to-end learning is general. Note that the results of G-TAD with TSM ResNet-18 are lower than those in BSP [21] and LowFi [22]. The reason might be the low resolution of videos.

It is interesting that the performance difference between TadTR and G-TAD increases when we switch from head-only learning to end-to-end learning. It indicates that traditional head-only learning might not be appropriate for benchmarking different approaches, as head-only learning restricts the performance of an approach.

Besides THUMOS14 and ActivityNet, we also evaluate the effect of end-to-end learning on HACS Segments [25]. As can be observed in Tab. A3, end-to-end training results in a performance gain of 6.42% in terms of average mAP. This again demonstrates the benefit of E2E learning and its generality.
The Effects of Image and Temporal Resolution on ActivityNet. Fig. A1 and Fig. A2 illustrate the effects of image resolution and temporal resolution (number of input frames) on ActivityNet, respectively. Similar to THUMOS14, increasing image resolution steadily boosts detection performance and also increases computation cost. As the image resolution reaches 96^2, the performance gain of increasing image resolution decreases. Compared to image resolution, the detection performance is less sensitive to temporal resolution. The reason might be that the action instances on ActivityNet are relatively longer than those on THUMOS14.

Besides, the action classes on ActivityNet are more related to scenes than motion. This observation also supports our choice of using sparse frame sampling on ActivityNet.

The Effect of Feature Fusion. In the I3D encoder, we fuse the features from the fourth stage and fifth stage. We verify the effectiveness of this strategy in Tab. A4. It is observed that this strategy improves the performance by 3%. It helps to compensate for the temporal information loss due to a decrease of the frame rate from 20 FPS to 10 FPS.

The Effect of the Frame Sampling Manner. By default, the encoder takes all frames from an input clip and extract feature in a temporally fully convolutional manner in our study. An alternative way is to sample fixed-length snippets one by one in a sliding-window manner and extract features for each snippet. The length of snippets is defined when the video encoder is pre-trained for action recognition (e.g., 8 for TSM). It is adopted by most works [9, 12, 23] based on offline features. However, it actually increases the total computation cost as adjacent snippets overlap with each other. In end-to-end training, we can still use this manner, at the expense of efficiency. Due to a high memory usage, we are only able to conduct the experiment with TSM ResNet-18 on 4 GPUs. As can be observed in Tab. A5, the snippet-wise manner actually gives lower performance than the fully convolutional manner, probability due to a limited temporal receptive field.
C. Computation Cost Analyses

Reasons for the Lower Computation Cost Than [13]. In Tab. 1 of the main document, we show that the detector built in this work is 126× faster (587ms vs. 74.1s) than the previous state-of-the-art non-end-to-end method [13]. The speed-up comes from three aspects. Firstly, we use a smaller image size and a lower frame rate. The setting of image size and frame rate in [13] is 224 × 224 and 30 FPS, while the setting in our case is 96 × 96 and 10 FPS. Secondly, we extract features in a fully convolutional manner instead of the conventional snippet-wise manner used in [13]. To be concrete, they use a sliding window strategy to sample snippets of 64 frames, which is the default input length of the I3D encoder, for feature extraction. The stride between two adjacent windows is 8 frames, 1/8 of the window length. Therefore, redundant computation is introduced. Finally, the encoder (SlowFast) and the head (TadTR) are more efficient than those in [13]. We note that these issues are not unique to [13]. They are prevalent in previous methods based on offline features and impede the application of TAD in real-world scenarios. We believe that end-to-end TAD can help eliminate these obstacles.

Computation Cost of Various Video Encoders. Except for the video encoders studied in the main paper, we analyze the computation cost of several video encoders used in previous end-to-end methods [16, 20] and the pre-training method [1] in Tab. A6. As they have different settings in temporal pooling, we adjust the number of sampled frames to ensure that they output features of the same length. We observe that C3D [18] and R(2+1)D-18/34 [19] are much heavier than SlowFast, although the former two have shallower backbones. For example, C3D, the fastest among them, is around 5× slower than SlowFast. They are less appropriate for temporal action detection. Therefore we do not use them in our experiments.

Training Time. Using the settings described in the implementation details, training SlowFast with TadTR head takes around 4 GPU hours on THUMOS14. On ActivityNet, the training time is around 11 and 1.5 GPU hours using SlowFast and TSM ResNet-18, respectively.

<table>
<thead>
<tr>
<th>mAP</th>
<th>FLOPs</th>
</tr>
</thead>
<tbody>
<tr>
<td>snippet-wise</td>
<td>34.25</td>
</tr>
<tr>
<td>fully-convolutional</td>
<td>36.12</td>
</tr>
</tbody>
</table>


References

[17] Deepak Sridhar, Niamul Quader, Srikanth Muralidharan, Xiaolin Li, Peng Dai, and Juwei Lu. Class semantics-based...
<table>
<thead>
<tr>
<th>Encoder</th>
<th>FLOPs (G)</th>
<th>Latency (ms)</th>
</tr>
</thead>
<tbody>
<tr>
<td>TSM R18 [8]</td>
<td>21.5</td>
<td>21.2</td>
</tr>
<tr>
<td>TSM R50 [8]</td>
<td>48.8</td>
<td>37.0</td>
</tr>
<tr>
<td>13D* [2]</td>
<td>83.2</td>
<td>62.1</td>
</tr>
<tr>
<td>SlowFast R50* [4]</td>
<td>41.4</td>
<td>51.2</td>
</tr>
<tr>
<td>C3D (VGG-11) [18]</td>
<td>906</td>
<td>277</td>
</tr>
<tr>
<td>R(2+1)D-18 [19]</td>
<td>960</td>
<td>382</td>
</tr>
<tr>
<td>R(2+1)D-34 [19]</td>
<td>1803</td>
<td>638</td>
</tr>
</tbody>
</table>

Table A6. Comparison of the computation cost of various video encoders, measured on video clips of 25.6 seconds. Image resolution: 96².

*Modified by us to make a temporal output stride of 4.


