CUF: Continuous Upsampling Filters

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Figure 1. We report (left) memory and FLOPs for upsampling an $256 \times 256$ image by different scale factors ($2 \times$, $3 \times$, $4 \times$) using integer scale upsamplers (Sub-Pixel Convolution [31] and CUF-instantiated) and arbitrary-scale upsamplers (Meta-SR [16], LIIF [6], LTE [19] and CUF), but the same encoder backbone (EDSR-baseline [21]); and (right) the relationship between each upsampler FLOPS and PSNR performance on DIV2k dataset. Our arbitrary scale model is significantly lighter than other methods in the same class (i.e. continuous super-res). Further, when instantiated for integer scale factors, our upsampler is even-more efficient than sub-pixel convolutions [31].

Abstract

Neural fields have rapidly been adopted for representing 3D signals, but their application to more classical 2D image-processing has been relatively limited. In this paper, we consider one of the most important operations in image processing: upsampling. In deep learning, learnable upsampling layers have extensively been used for single image super-resolution. We propose to parameterize upsampling kernels as neural fields. This parameterization leads to a compact architecture that obtains a 40-fold reduction in the number of parameters when compared with competing arbitrary-scale super-resolution architectures. When upsampling images of size $256 \times 256$ we show that our architecture is 2x-10x more efficient than competing arbitrary-scale super-resolution architectures, and more efficient than sub-pixel convolutions when instantiated to a single-scale model. In the general setting, these gains grow polynomially with the square of the target scale. We validate our method on standard benchmarks showing such efficiency gains can be achieved without sacrifices in super-resolution performance. https://cuf-paper.github.io

1. Introduction

Neural-fields represent signals with coordinate-based neural-networks. They have found application in a multitude of areas including 3D reconstruction [24], novel-view synthesis [32], convolutions [14], and many others [37].

Recent research has investigated the use of neural fields in the context of single image super-resolution [6, 19].1 These models are based on multi-layer perceptrons conditioned on latent representation produced by encoders. While such architectures allow for continuous-scale super-resolution, they require the execution of a conditional neural field for every pixel at the target resolution, making them unsuitable in applications with limited computational resources. Further, such a large use of resources is not justified by a increase in performance compared to classical convolutional architectures such as sub-pixel convolutions [31]. In summary, neural fields have not yet found widespread adoption as classical solutions are ① trivial to implement and ② more efficient. As they generally perform compa-

1Thereon we assume single image when talking about super-resolution.

2These encoders were originally proposed for classical super-resolution applications, and include both convolutional [8, 21, 40] as well as attentional [7, 20, 39] architectures.

https://cuf-paper.github.io
rably, their usage is not justified in light of these points; see Figure 1. In this paper, we focus on overcoming these limitations, while noting that (regressive) super-resolution performance is in the saturation regime (i.e., further improvements in image quality seem unlikely without relying on generative modeling [30], and small improvements in PSNR do not necessarily correlate with image quality).

Our driving hypothesis is that super-resolution convolutional filters are highly correlated — both spatially, as well as across scales. Hence, representing such filters in the latent space of a conditional neural field can effectively capture and compress such correlations. Given that neural fields encode continuous functions within neural networks, we call our filters Continuous Upsampling Filters (CUFs). While neural fields have proven effective in parameterizing 3D convolutions [5, 14, 36], we demonstrate that there are very significant savings to be had in the parameterization of 2D convolutions.

In implementing continuous upsampling filters, we draw inspiration from sub-pixel convolutions [31], and realize them via depth-wise convolutions. This not only makes CUFs significantly more efficient than competing continuous super-resolution architectures, but when instantiated to single-image super-resolution they are, surprisingly, even more efficient than “ad-hoc” sub-pixel convolutions with same number of input and output channels; see Figure 1.

Contributions. We investigate the use of neural fields as a parameterization of convolutional upsampling layers in super-resolution architectures, and show how:

- The continuity of neural-fields leads to training of compact convolutional architecture for continuous super-resolution (similar super-resolution PSNR but with flop reduction that grows polynomially with the square of the target scale.).
- Instantiating our continuous kernels into their discrete counterpart leads to an efficient inference pipeline, even more efficient in number of operations per target pixel than ad-hoc sub-pixel convolutions with same number of input and output channels.
- Discrete cosine transforms can be used as an efficient replacement for Fourier bases in the implementation of positional encoding for neural fields.
- These gains do not hinder reconstruction performance (PSNR), by carefully validating our method on standard benchmarks, and thoroughly ablating our design choices.

2. Related work

A thorough coverage of single-image super-resolution can be found in the following surveys [2, 3, 35]; in what follows, we provide an overview of classical techniques for super-resolution based on deep learning. As our model is based on neural-fields, we also point the reader to a survey in this topic [37], and below we discuss the existing works for super-resolution based on neural fields.

Single scale. There are two main frameworks for super-res, which mainly differ in the placement of upsampling operators within the architecture.

- In the pre-upsampling framework [8], an initial upscaled image is obtained using a non-trainable upsample (e.g., bicubic), which is then post-processed by a neural network. This enables arbitrary size/scaling, but can introduce side effects such as noise amplification and blurring.
- In the post-upsampling framework [31], an encoder that preserves the input spatial resolution is followed by a shallow upsampling component.

Note the computational complexity of pre-upsampling is significant, as these model operate directly at the target resolution. Consequently, post-upsampling is the de-facto mainstream approach [9, 20, 21, 34, 39, 40]. For this reason, we investigate the impact of an implicit upsample in post-upsampling frameworks, and cover a diverse set of architectures ranging from large [22], to extremely lightweight models [10].

Neural Fields. The application of neural fields to super-resolution [16] has enabled the design of architectures that support multiple target scales, as well as non-integer super-resolution scales.

- In meta super-resolution MetaSR [16], a hyper-network is conditioned on the target scale factor and on the output pixel relative coordinates (i.e. the difference between source and target grids covering the same normalized global space). Their model consists of: (1) transforming the input image using an encoder into deep features at the source resolution; (2) upsampling the deep features into the target space (using nearest-neighbors); (3) using the hyper-network to produce a filter set per target relative position and scale; (4) processing the enlarged target image.
Table 1. Super-resolution with neural-fields: An overview of the main characteristics of neural field based architectures. Square brackets represent concatenation. In LTE [19], $h_a, h_b$ are $3 \times 3$ convolutional layers and $h_c$ is a linear layer.

![Diagram](image)

Figure 3. Architecture: Continuous Upsampling Filters (UF) are based on a hyper-network. For each pixel position in the highres space, the hyper-network is computed $K^2$ times, once for each position $i,j$ of the upsampling kernel (batched in practice). This can be reduced when upsampling with integer scales $s$, as is common in practice. The hyper-network output is aggregated into a tensor that is then multiplied against the unfolded nearest-neighbor interpolation, computing a depth-wise convolution with per-pixel filters. The number of encoder channels, $C_e$, is dependent on the encoder used. In our experiments, we set the number of hyper-network channels, $C_h = 32$.

resolution feature map into the final output using the corresponding weights.

- In local implicit image function LIIF [6], the author eliminates the use of a hyper-network (MetaSR, step 3) by extending the sampled target resolution features (step 2) with extra channels representing the relative coordinates and target scale. Their combined features are further processed through a stack of five dense layers inspired by the multi-layer perception (MLP) architecture typically adopted as the end-to-end model in neural-fields formulations.

- In local texture estimator LTE [19] the authors enhance the features fed as input to the MLP layers. In their model, the output of the encoder is processed by three extra trainable layers associated with the amplitude, frequency, and phase of sin/cosine waves. The resulting projection in the target resolution layer is used as the input to an MLP that stacks 4 fully-connected layers. In order to further improve the results they also adopt a global skip connection with a bilinear up-scaled version of the input around the full model, such that the deep model focus on the computation of the residual between to the closed form approximation and the final result.

A visualization of these architectures can be found in Figure 2. In practice, LIIF and LTE also increase the number of feature channels as their MLP layers have more neurons (256) than the number of channels produced as the encoder output (64 for encoders such as EDSR [21] and RDN [40]). In contrast to these previous works, our upsampler:

- Moves the computational burden of arbitrary upsampling into the continuous upsampler operator while reducing the operations performed in the target resolution space, preserving the number of channels produced by the deep encoder (Table 1).

- Requires fewer operations than the single-scale sub-pixel convolution in the most typical application case of upsampling an image by an integer scale factor, and producing results of comparable quality.

- Adopts a neural-fields formulation that focuses the sensitivity of our hyper-network to fine-grained changes in the representation of scale and relative position (reducing spectral bias).

3. Method

Given a target scale $s$, we aim to produce an up-scaled image of size $sH \times sW \times 3$ given an input image of size $H \times W \times 3$. Within the context of super-resolution, our core contribution is the introduction of a novel learnable upsampling layer. The upsampling layer can be interpreted as a decoder in a classical encoder-decoder architecture. We briefly review our encoder architecture in Section 3.1, and detail our decoder in Section 3.2. Note that our analysis focuses on the decoder, as a variety of encoders can be used. In Section 3.4, we then perform a conceptual comparison between our architecture, shown in Figure 3, and others based on neural fields, which are visually summarized in Figure 2.

Training. Our network with parameters $\theta$ is trained to regress a high resolution image $\hat{I}$ matching the ground truth $I_{gt}$ at random positions $x$ given the low resolution input image $I$:

$$\arg\min_{\theta} E_{I} E_{x} E_{\theta} \left\| \hat{I}(x; I, \theta) - I_{gt}(x) \right\|^2_2$$  (1)
where \( \mathbf{x} \) indexes pixel locations in the target resolution, and \( \delta^k(\mathbf{x}) = \text{mod}(\mathbf{x}, s)/s \) renders our convolutional filters translation invariant, and the dot product implements a spatial convolution. The network \( D \) is a point-wise layer that maps (super-resolved) features back to RGB values:

\[
D_\theta : \mathbb{R}^C \rightarrow \mathbb{R}^3
\]  

(6)

This is applied to a grid of coordinates of size \( \mathbb{R}^{sH \times sW \times C} \) to get an image of size \( \mathbb{R}^{H \times W \times 3} \). The coordinate \( \mathbf{x} \) is continuous, hence \( \mathcal{K}_\theta \) is a neural field parameterization of a convolutional kernel mapping (continuous) spatial offsets and (continuous) scales to convolutional weights:

\[
\mathcal{K}_\theta : [0, 1]^2 \times \mathbb{R}^+ \rightarrow \mathbb{R}^{C \times K^2}
\]  

(7)

**Continuous kernel indexing.** We can take our continuous formulation a step further by introducing a continuous parameterization of its kernel indexes \( k_1, k_2 \) indexing our \( K \times K \) convolution weight entries. Thus, the hyper-network representing the convolution field becomes:

\[
\hat{\mathcal{K}}_\theta : [0, 1]^2 \times \{0, \ldots, K - 1\} \times \mathbb{R}^+ \rightarrow \mathbb{R}^C
\]  

(8)

and \( \mathcal{K}_\theta \) can then be constructed by \( 9 \) invocations to \( \hat{\mathcal{K}}_\theta \) in the case of a \( K = 3 \), and \( K^2 \) invocations in the general setting. We use \( K = 3 \) throughout the paper in order to facilitate a direct comparison to pre-existing baseline models.

Equation (7) and Equation (8) are both valid ways of generating spatial kernels. The first can be seen as a multi-headed hypernetwork, while the second uses input conditioning to generate the kernel values. We use the latter in our experiments, as the layers of nonlinearities provide additional expressiveness compared to the linear transformation used in the multi-headed version. A comparison between the two is presented in Appendix D.

### 3.1. Encoder

Towards this objective, we first process our input image with an encoder producing \( C \)-dimensional features:

\[
\mathcal{E}_\theta : \mathbb{R}^{H \times W \times 3} \rightarrow \mathbb{R}^{H \times W \times C}
\]

(2)

and then unfold a \( k \times k \) spatial neighborhood of \( C \)-dimensional features into a tensor of \( C \times k^2 \) channels. Finally, we upsample the encoded image using nearest-neighbour interpolation. We define this step as:

\[
\mathcal{U} : \mathbb{R}^{H \times W \times C} \rightarrow \mathbb{R}^{sH \times sW \times C \times k^2}
\]

(3)

The unfolding part of this procedure leads to feature maps that will allow us to implement depth-wise, spatial convolutions as dot products, as is typically done for low-level neural network implementations. We define this whole feature extraction procedure as:

\[
\mathbf{F} = \mathcal{U}(\mathcal{E}_\theta(\mathbf{I}))
\]

(4)

**3.2. Decoder – “Continuous Upsampling Filters”**

We draw our inspiration from classical sub-pixel convolutions [31], and combine it with recent research that apply neural fields for continuous super-resolution [6, 19]. Differently from these architectures, we achieve this objective by instantiating upampling kernels \( \mathcal{K} \) via hypernetworks [13]. Our super-resolution network is algebraically expressed as:

\[
\tilde{\mathbf{I}}(\mathbf{x}; I, \theta) = D_\theta(\mathcal{K}_\theta(\delta^k(\mathbf{x}); s) \cdot \mathbf{F}([\mathbf{x}/s]))
\]

(5)

### 3.3. Positional encoding

Naively representing signals with MLPs leads to “spectral bias” [26]: the overall difficulty these networks have in representing high-frequency signals. Hence we implement \( \mathcal{K}_\theta \) as:

\[
\mathcal{K}_\theta(\delta^k(\mathbf{x}), s, k) = \text{MLP}(\Pi(\delta^k(\mathbf{x})), \Pi(s), \Pi(k))
\]

(9)

where we apply positional encoding \( \Pi(\cdot) \) to the MLP inputs. Several variants of positional encoding exist, including Fourier [24] and random Fourier [32] variants. However, as we strive for efficiency, we take inspiration from classical signal processing (e.g. JPEG) and instead employ a cosine-only transformation. Given a scalar \( f_n \) sampled uniformly in the range \( [0, f_{\text{max}}] \), a scalar quantity \( z \in [0, 1] \) is encoded as the (sorted) vector:

\[
\Pi(z) = \left\{ \cos \left( \frac{(2z + 1)f_n \pi}{2} \right) \right\}_{n=1}^{N}
\]

(10)
Table 2. Out-of-domain evaluation – performance on datasets not seen during training. Metrics computed on the luminance channel in the YCbCr color space, following previous work. Models using self-ensemble are marked with $+_{\text{geo}}$ and models using local self-ensemble are marked with $+_{\text{loc}}$. Legend: best 2nd best

<table>
<thead>
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<th>Upsampler</th>
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<th>Set14</th>
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<tr>
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<tr>
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<table>
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<td>$+$ geo</td>
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<td>31.39</td>
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Table 4. Multi-scale up-sampling methods – DIV2K

<table>
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<th>Ens.</th>
<th>$[H \times W \times s^2]$</th>
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</thead>
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<td>$+$ loc</td>
<td>$+$ geo</td>
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</table>

Figure 6. A simple illustration of periodic shuffling used in Sub-Pixel Convolution.

that generates a feature map with $s^2 N_{out}$ channels, where $N_{out}$ is a hyper parameter defining the target number of channels of the Sub-Pixel Convolution operation. Next, the periodic shuffling operator $\sigma$ rearranges the channels to produce a higher-resolution feature map (see Figure 6):

$$P : \mathbb{R}^{H \times W \times C} \rightarrow \mathbb{R}^{sH \times sW \times C \times s^2}$$

(11)

Similar to (6), whenever $N_{out}$ is chosen as different from the number of output colors, the resulting feature map is further projected by point-wise convolutions. Often, $N_{out}$ is taken as the same as the number of channels as produced by the encoder $E$ for architectures adopting Sub-Pixel Convolution and targeting high quality results [20,21,40]. We note that the Sub-Pixel Convolution design based on the expansion convolution does not enforce spatial correlations between neighboring pixels (here we refer to neighborhood in the output, high resolution, domain) and need to be learned from training data. Conversely, these correlations are inherently captured by our continuous convolutional filters.

Further, in the integer scale setup where hyper-networks can be pre-instantiated, CUFs are computationally more efficient than sub-pixel convolutions whenever $K^2 + N_{in} + N_{out} < N_{out} \times K^2$ (in the worst case scenario) as the cost associated with pre-computing the weights for a given integer up-sampling scale can be neglected when compared to the operations performed on the image grid (Appendix E).

Comparison to LiIF [6] and LTE [19]. In comparison to these works, we make the main network shallower (1 depth-wise continuous convolution and 2 dense layers) in order to

3 also called pixel-shuffle (PyTorch) or depth-to-space (TensorFlow).
reduce the number of layers that operate at the target spatial resolution and at the encoder’s channel resolution. At the same time, our upsample head is considerably lighter than previous arbitrary scale methods, not only by the use of a depth-wise convolution but also by keeping the same number of channels produced by the features encoder in the main network \( C_e = 64 \), which results in dense layers \( 16 \times \) cheaper than its LIIF and LTE counterparts using 256 neurons). When performing non-integer upsampling, the costs with processing the hyper-network layers are proportional to the target image resolution, but still smaller than a single dense layer of LIFF and LTE heads, due to the adoption of a reduced number of channels (as \( C_h = 32 \), each of CUF’s hypernetwork dense layers is \( 64 \times \) cheaper than LIIF and LTE layers using 256 neurons).

**Instantiating CUFs.** At inference time, when targeting an integer upsampling factor \( s \), the hyper-network representing \( K \) can be queried to retrieve the weights corresponding to \( s^2 \) relative subpixel positions as an initialization step during pre-processing. The retrieved weights are re-used across all pixels taking advantage of the existent periodicity. Thus, in the CUF-instantiated architecture the continuous kernel is replaced at test time with a discrete depth-wise convolution, followed by a pixel shuffling operation, in contrast with the unfolding operator used in the regular, fully continuous CUF described in this paper. Otherwise, the architecture remains the same. The costs associated with the hyper network at initialization can be neglected as \( s^2 \ll sH \times sW \). In this setting, our model becomes as efficient as a sub-pixel-convolution architecture, while retaining the aforementioned continuous modeling properties at training time.

### 4. Results

In this section we describe our experimental setup in terms of backbone encoders and training/validation dataset, perform careful comparisons to the state-of-the-art (Section 4.1), and additional evaluation towards the implementation of lightweight super-res architectures (Section 4.2). We conclude by performing a thorough ablation (Section 4.3).

#### Encoders.

We apply CUFs to a variety of encoders, to both show its generality, as well as its performance gains in a variety of settings.

- Well-known convolution-based encoders, namely EDSR-baseline [21] and Residual Dense Networks (RDN) [40].
- Architectures for lightweight and extremely lightweight inference, respectively SwinT-lightweight [20] and ABPN [10].

Table 4 contrasts the ablated encoders’ composition and parameters.

<table>
<thead>
<tr>
<th>Method</th>
<th>Composition</th>
<th>Parameters (in M)</th>
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<tr>
<td>RDN</td>
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<tr>
<td>SwinT</td>
<td>Conv. and Self-Attention</td>
<td>11.60</td>
</tr>
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<td>EDSR-baseline</td>
<td>Convolutions</td>
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<tr>
<td>SwinT-lightweight</td>
<td>Conv. and Self-Attention</td>
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<tr>
<td>ABPN</td>
<td>Convolutions</td>
<td>0.03</td>
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</table>

Table 4. **Encoders** – Our experiments use CUF layers in combination with various encoders differing in composition and size. Parameter counts reflect the encoder size and the upsampling head using a Sub-Pixel Convolution (spc) targeting \( 4 \times \) upsampling size. Hyperparameter settings can be found in the appendix.

#### Datasets.

All models are trained using the DIV2K dataset training subset (800K images), introduced at the NTIRE 2017 Super Resolution Challenge [25]. Similarly to previous works, each image is randomly cropped 20 times per epoch and augmented with random flips, and 90 degrees rotations. We report peak signal-to-noise ratio (PSNR) results on the DIV2K validation set (100 images)\(^4\), in the RGB space. To understand the generalizability of our experimental results, we test our models on additional well known datasets (Set5 [4] – 5 images; Set14 [38] – 14 images; B100 [23] – 100 images; and Urban100 [17] – 100 images), in which, following previous works, PSNR is measured in the YCbCr luminance channel.

#### Ensembling.

Whenever demarked by ’+geo’, results incorporate the (commonly adopted) Geometric Self-ensemble [21], where the results of rotated versions of the input images are averaged at test time. LIIF and LTE results adopt Local Self-ensemble (’+loc’), where the result of applying the upsampler at \( 4 \) shifted grid points is averaged, resulting in a \( 4 \times 4 \) increase in computational complexity at training/test time. It may also introduce confounding factors to the optimization process that invalidate direct comparison to the fixed-scale upsamplers, as during backpropagation the encoder gradients are averaged. In Appendix B we combine these models.

### 4.1. State-of-the-art comparisons

Table 2 and Table 3 show the quantitative comparisons, while qualitative results can be observed in Figure 4, Figure 7 and Appendix F as well as on our supplementary website [https://cuf-paper.github.io/]. Table 3 contains in-domain results: models trained on DIV2k and tested on DIV2k (train/test split), while Table 2 presents out-of-domain results: models are trained on DIV2k, but tested on different datasets. Note that upsampling ratios larger than \( \times 4 \) are not provided at training time. The models adopting sub-pixel convolutions are fixed-scale models, that is, one model is trained per target scale and thus do not

\(^4\)The models we compare to also do not report results on the test-set, which is no longer publicly available.
Figure 7. Qualitative evaluation – CUF combined with different encoders and upsampling scales.

Figure 8. Residuals – between ground truth and (from left to right) results from LIIF, LTE and CUF shown in Fig 7.

generalize to unseen or fractional scales. On the remaining rows we present results of arbitrary-scale upsamplers (MetaSR [16], LIIF [6], LTE [19]), where a single model is trained and tested across different upsampling ratios. Our method is the first arbitrary-scale method to match (or surpass) fixed-scale performance across all encoders in both single-pass inference and self-ensemble scenarios.

Table 5. Upsampling (256x) on a A100 GPU. Wall clock (wc) (i.e., considering both CPU and GPU execution) and GPU time in ms averaged over 1k runs.

4.2. Lightweight super-resolution

The encoders and upsamplers considered so far are not designed to be run on devices with limited memory and power. Running super-resolution algorithms on mobile devices comes with unique challenges due to limited RAM and non-efficient support for certain common operations [18]. The proposed CUF-based upsampler, in combination with a mobile-compatible encoder, can be effectively used for upsampling on mobile devices.

To test the effectiveness of the CUFs, we adopt a lightweight version of SwinIR (0.9M parameters) [20] as well as one of the best performing super-lightweight architectures for mobile devices (30K parameters) [10] as the encoder. For the latter, the original architecture (Figure 9, left) consists of a lightweight encoder and an upsampling module consisting of a series of convolutions, ReLU's, and a standard periodic shuffle layer. We replace this upsampling module with a CUF-based one (Figure 9, right). With instantiated CUFs, this means simply introducing a depthwise convolution followed by pointwise layers, where the weights are output by the learned CUFs.

Table 6 illustrates that CUFs provide the same level of accuracy while enabling continuous scale and efficient inference. To our knowledge, this is the first mobile-friendly continuous scale super-resolution architecture.

4.3. Ablations

We run several ablation experiments in order to analyze the effects of various model choices. Here, we detail our choice of using DCT for positional encoding vs Fourier features, as well as an investigation of the redundancy of CUF filters versus subpixel convolution. An analysis on the various conditioning factors of our neural fields (positional en-
<table>
<thead>
<tr>
<th>Encoder</th>
<th>Upsampler</th>
<th>Set5</th>
<th>Set14</th>
<th>BSD100</th>
<th>Urban100</th>
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<tbody>
<tr>
<td>SwinIR-Right [29]</td>
<td>Sub-Pixel Conv</td>
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<td>34.54</td>
<td>32.35</td>
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</tr>
<tr>
<td></td>
<td>CUF (ours)</td>
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<table>
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<th>Method</th>
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</tr>
<tr>
<td>DCT</td>
<td>30.44</td>
<td>26.86</td>
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</table>

Table 6. **Lightweight super-res** – Our CUFs enable continuous super-resolution with lightweight (SwinIR-lightweight [29]) and mobile-friendly (ABPN [10]) backbones without quality loss.

Table 7. **Cosine basis** – matching results but using half of sinusoidal projections and neurons at the hyper-network input. PSNR from Urban-100 dataset using an ABPN encoder.

Positional encoding. A large body of work uses a Fourier basis for the positional encodings, e.g. [24, 33]. However, we opt to use a DCT basis as this requires half of the sinusoidal projections and neurons at the hyper-network first layer. This trick has been used in other domains, such as random approximations of stationary kernel functions [27].

Table 7 shows the PSNR, as obtained using a DFT versus a DCT basis, and verify that we do not observe loss in performance, while at the same time saving half of the computation associated with positional encoding operations.

Redundancy of filters. As noted in Section 3.4, sub-pixel convolution does not enforce spatial correlation between neighboring weights in its convolutional filters, but has to learn them from data. CUF on the other hand generates the filters with a hypernetwork, which intrinsically builds on smoothness due to its functional form. Here, we test this hypothesis on the filters of CUF and sub-pixel convolutions. For this analysis, we use an EDSR encoder with $C_e = 64$, $K = 3 \times 3$, and $s = 3$. Subpixel convolution maps $C_e$ features to $s^2C_e$ features, and each group of $s^2$ output features are rearranged to form the upsampled feature space. We therefore consider each group of $s^2$ filters of size $K \times K \times C_e$ and do an eigenvalue analysis to determine the compressibility of these filters. This is done by forming $C_e$ separate matrices of size $s^2 \times C_e K^2$ and performing PCA on each of them separately, forming $64$ separate sets of eigenvalues. We plot this distribution for trained and untrained sub-pixel layers in Figure 10a. A faster decay means more redundancy, as more variance is explained by fewer components. In this case, training adds significant structure to the filters.

Conversely, each output channel in CUF is computed by $s^2$ filters of size $K \times K$. These are not directly comparable to subpixel convolution, but we perform an analogous eigenvalue analysis on trained and untrained CUFs. As shown in Figure 10b, unlike subpixel convolution, CUFs actually become less redundant with training, suggesting that the initial hypernetwork already imposes a very smooth prior on the filters. In other words, CUF filters incorporate spatial correlations from the very beginning due to their functional form.

5. Conclusions

We propose CUF, a computationally efficient modeling of continuous upsampling filters as neural fields. Different from previous arbitrary-scale architectures, the performance gains obtained by our upsampler are not due to increased capacity but rather to a more efficient use of model parameters. A single hyper-network supports filter adaptation across scales while at the same time CUF’s upsampling head has fewer parameters than previous arbitrary-scale heads, as well as fewer parameters than a single Sub-Pixel Convolution head at $4 \times$ upsampling. CUF is the first arbitrary scale model that can be effectively used for upsampling on mobile devices, an area previously dominated by single scale Sub-Pixel Convolution architectures. CUF’s architecture is quite general. A natural direction of future work is to investigate its use for architectures with multiple upsampling layers (and the reuse of filters across scales), for different image generation settings such as GANs [12] and diffusion models [15]. Neural fields have done particularly well on higher-dimensional signals. Another line of future work is the use of CUF for efficient super-resolution of video data. Continuous convolution filters are an exciting and increasingly studied area of research [28, 29] with many applications as an alternative to traditional convolutions.

Overall, the presented results aligned with low computational resources suggest CUF offers a practical modeling of continuous filters.

![Subpixel Convolutions](image1.png)

![CUF](image2.png)

Figure 10. CUF depthwise continuous filters are low rank by construction. Subpixel-convolution fails to find low rank filters.
References


