



# GDP: Stabilized Neural Network Pruning via Gates with Differentiable Polarization

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#### **Abstract**

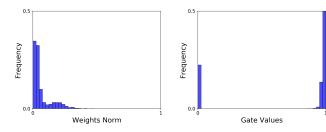
Model compression techniques are recently gaining explosive attention for obtaining efficient AI models for various real time applications. Channel pruning is one important compression strategy, and widely used in slimming various DNNs. Previous gate-based or importance-based pruning methods aim to remove channels whose "importance" are smallest. However, it remains unclear what criteria the channel importance should be measured on, leading to various channel selection heuristics. Some other sampling-based pruning methods deploy sampling strategy to train sub-nets, which often causes the training instability and the compressed model's degraded performance. In view of the research gaps, we present a new module named Gates with Differentiable Polarization (GDP), inspired by principled optimization ideas. GDP can be plugged before convolutional layers without bells and whistles, to control the on-and-off of each channel or whole layer block. During the training process, the polarization effect will drive a subset of gates to smoothly decrease to exact zero, while other gates gradually stay away from zero by a large margin. When training terminates, those zero-gated channels can be painlessly removed, while other non-zero gates can be absorbed into the succeeding convolution kernel, causing completely no interruption to training nor damage to the trained model. Experiments conducted over CIFAR-10 and ImageNet datasets show that the proposed GDP algorithm achieves the state-of-the-art performance on various benchmark DNNs at a broad range of pruning ratios. We also apply GDP to DeepLabV3Plus-ResNet50 on the challenging Pascal VOC segmentation task, whose test performance sees no drop (even slightly improved) with over 60% FLOPs saving.

#### 1. Introduction

Model compression techniques recently attract many attentions from both industry and academic research communities for compressing Deep Neural Networks (DNNs) to satisfy devices with limited computing and storage capabilities. One common model compression method is pruning, which includes unstructured pruning (i.e., weights sparsification) [4, 20, 38, 46, 66] and structured pruning (e.g., channel pruning and kernel pruning) [78]. Unstructured pruning removes individual weight, which results in sparse connection and requires sparse matrix operations supports. The structured pruning eliminates an entire neuron or a block at once, leaving the model's structure and connections intact and thus having no special requirements for hardware or specific libraries. In this paper, we focus on the latter.

Importance-based pruning methods usually prune channels/blocks whose importance are small, but it remains unclear what criteria the importance should be measured on, leading to various importance selection heuristics, such as the magnitude of weights [74], first-order or second-order information of weights [11, 56, 59], knockoff features of filters [69] and so on. One recent track of works follow the sparse learning optimization routines, utilizing LASSO or group LASSO [24, 48, 74] to regularize training weights. However, these methods need to prune from a skewed yet continuous histogram of weights values, and the selection of the pruning threshold [20] is critical yet ad-hoc, taking Figure 1(a) for example. Moreover, removing many small yet non-zero weights will inevitably damage the network, and often need to fine-tune before the next pruning schedule, causing iterative pruning-finetuning loop [19, 42, 48, 55].

Besides the importance-based pruning methods, some sampling-based methods [14, 34, 53, 87, 22, 84] either sample a sub-network or calculate expectations over whole network to train, utilizing Gambul-Softmax [31], STE [1] or Bernoulli distribution [66, 84] strategies. These methods may potentially cause either unstable training due to equiprobable sampling (i.e. sample a completely different sub-net when the probability is nearly uniformly distributed at the beginning of training), or converging at local minimum due to deterministic sampling (i.e. sample nearly the same sub-net when the probability becomes deterministic), thus need various ad-hoc manual designs [14].



(a) Distribution of weights in LASSO (b) Distribution of gates in GDP Figure 1. The density distribution of weights norm for LASSO-based pruning (left) and gates value for GDP pruning (right). LASSO is performed on the dimension of input channel and all the norm is divided by the number of entries then scaled to  $0\sim1$ . The Y-axis is truncated to 0.5 for better visualization. We can see that the LASSO method yields a continuous distribution and needs to carefully choose a small threshold to prune the neurons. While in GDP, some gates values are exact zero while the others are away from zero by a large margin. The experiment is conducted on ImageNet with MobileNet-V1.

FLOPs ratio	Before removing	After removing	After fine-tuning
43.8%	69.962	69.962	70.150

Table 1. This table shows there is no performance drop before and after removing zero-gated channels/blocks using GDP. This experiment is conducted on ImageNet [9] with MobileNet-V2 [64].

To bridge these research gaps, we introduce a novel scheme called *Gates with Differentiable Polarization* (GDP), which can easily plug-and-play with any existing convolutional or fully connected layer. It does not rely on any specific module such as Batch Normalization (BN) [29] or ReLU [15], and contains fewer hyper-parameters to tune during optimization than those minimax optimization methods [41, 77] used in resource-constrained model compression. The core idea of GDP is to encourage gate values to update smoothly towards polarization (some are **exact zero**, others stay away from zero by a large margin) during training, so that the exact-zero-gated channels/blocks can be painlessly removed and other gates be absorbed into conv/fully-connected kernels, without causing any damage, as illustrated in Figure 1(b) and Table 1.

Our main highlights are summarized as follows:

- Core Idea of Polarization: GDP is designed to push
  a large interval between exact zero gates and non-zero
  gates while preserving the gradient-friendly optimization. This is particularly favored, and enables us to directly remove zero-gated channels/blocks and absorb
  other gates to the conv/fully-connected kernels with no
  performance impact when training terminates.
- Stability, Consistency and Light-Weight: GDP gets rid of ad-hoc thresholding nor any probabilistic sam-

pling/expectation. That makes it (1) easy to use, with nearly no manual crafting; (2) produce stable and smooth training; (3) incur almost no computational or time overhead; and (4) not rely on any specific modules such as BN or ReLU.

State-of-the-Art Results: Experiments on CIFAR-10 and ImageNet classification tasks show that GDP outperforms all previous competitors with clear margins. GDP can obtain slightly improved performance in the Pascal VOC segmentation task with DeepLabV3Plus-ResNet50, while reducing more than 60% FLOPs.

#### 2. Related Work

Model compression techniques are broadly applied in many research fields successfully, such as GAN [70, 13], Recommendation system [65] and so on. There are many model compression techniques proposed to solve different problems, including network pruning [28, 48, 88], weights quantization [30, 32, 35], knowledge distillation [25, 27], architecture searching [47, 90], weights hashing [12] and so on. Some works [19, 61, 71, 76, 16] have tried to unify aforementioned techniques into one framework, which can slim network comprehensively. In this paper, we only focus on pruning strategy to achieve SOTA compression results by leveraging a differentiable polarized gate design and we want to emphasize that our GDP method can be adjusted into a unified compression framework easily.

#### 2.1. Pruning Strategies

Importance based pruning: One straightforward way to prune a network is to throw out less important components. [24, 42, 43, 48, 74] use group LASSO to regularize convolution kernels or other scale factors during training, leaving some weights values small. However, these works suffer from how to choose pruning threshold. Furthermore, [80] points out that channels or blocks with smaller weights norm may not necessarily means that they are less important. Besides weights norm metric, some other works [7, 39, 56, 58, 38, 52] explore some new importance metrics, such as first-order, second-order taylor expansions of weights and so on. The designing of importance metrics is still an open problem to explore.

**Sampling based pruning:** Sampling process is not differentiable, however [22, 34] use Gumbel-Softmax [31] trick to make sampling process differentiable during network training, thus can be used in pruning network. While [14] uses straight-through estimator [1] on a step function controlled by probability oscillating around 0.5. [51, 84] relaxes the intractable L0 regularization by using hard-sigmoid function as gate function instead, then sample a sub-net by Gumbel-Softmax similarly as previous work or

by Bernoulli [84]. These methods sample sub-networks stochastically, while we continually adapt a single network. DARTS [47] introduced differentiable architecture search by formulating the output of a layer as the expectation of all input candidate edges and then choosing edges with the largest probabilities, which can be treated as a general pruning method. We do not want to compute expectation, since it cannot accurately depict real sub-net during training.

Other variants: Beyond above two categories, several works choose to prune networks gradually by reinforcement learning [23, 68]; meta-learning [49]; evolutionary methods [18] and so on. Furthermore, [83] trains a proxy network to represent the accuracy of sub-nets with different channel configurations, and then greedily slim a network. [3] considers a generalized pruning on all dimensional of network to progressively train once for all sub-nets. All these methods may suffer from the computational resources overhead and their complicated designs may hinder the popularization and application of them. However, our pruning method is straightforward and takes about the same amount of time as training of baseline model.

A series of recent works [41, 62, 77, 85] formulate the network pruning problem into a constrained minimax optimization problem, and then use ADMM method [2] to solve it. However, these methods inevitably introduce many hyper-parameters (like dual variables) to tune. Instead, our method is simple yet efficient, with fewer parameters to tune during compression.

## 3. Methods

We introduce the details of our pruning algorithm in this section, including a special gate design, the FLOPs reformulation based on it and the proposed alternative proximal optimization method.

# 3.1. Pruning via Gates

For CNNs compression, instead of directly measuring the channel importance as in previous work, we plug trainable gates immediately before regular convolution ('regular' is used to distinguish from 'depth-wise') or fully connected layers to control the on-and-off of each channel or even whole block. Some works [34, 80, 82] utilize BN or ReLU layer as gates, however, a channel being judged as unimportant now doesn't mean it won't be important after a series of channel-wise operations, such as pooling, depth-wise convolution [8], *et al.* What's more, some networks may not contains BN or ReLU. So, we plug gates immediately before the position where information from different channels are going to merge. Different from [14], we do not plug gates before depth-wise convolution, which does not merge information from different channels. Figure 2

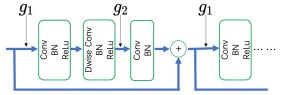


Figure 2. Take a residual-block as example to show the position where the gates plug into. The gates are plugged immediately before regular convolution layers but not depth-wise convolution. Note that  $g_1$  appears twice to ensure consistency of the two convolution kernels connected by skip connection. If all the entries of  $g_2$  become zeros, the entire block can be removed (except for the bias term in the BN).

illustrates the positions where the gates should be plugged into, without losing generalization, simply taking a residual block in MobileNet-V2 as example.

Specifically, let  $X_{l-1} \in \mathbb{R}^{c \times h \times w}$  be the input of the l-th layer convolution kernel  $W_l \in \mathbb{R}^{c \times d \times r \times r}$ , where c, h and w represent the number of channels, height and width of the input feature map respectively; d and r is the number of output channels and kernel size of the convolution kernel, respectively. We can insert a gate vector  $\bar{g}_l = \{g_l^{(1)}, g_l^{(2)}, ..., g_l^{(c)}\}$  with dimension c as channel-wise scaling factors for X, before it is convolved by W. That is, the output,  $Y_l \in \mathbb{R}^{d \times h \times w}$ , of the convolution operation is

$$Y_l = W_l * (\bar{q}_l \odot X_{l-1}) \tag{1}$$

where  $\odot$  is the channel-wise multiplication, and \* is the convolution operation. One can easily control the on-and-off of these channels by manipulating  $\bar{g}_l$ . In GDP, some gates smoothly decrease to be exact zeros, while others stay away from zeros by a large margin gradually. Then we can safely remove channels corresponding to the zero-gates, and absorb the others to the successive convolution kernel. By this way, the sub-net can be got with performance same to the super-net.

## 3.2. Differentiable Polarized Gates

Previous gate-based methods usually use some optimization tricks to make the gate differentiable, such as Gumbel-Softmax trick used in sampling-based method and L1-norm penalty in regularization based method and so on. These methods either cause unstable training process or cannot obtain exact zero values. To ensure the stability and effectiveness of model pruning, the desired gate should have following properties: 1. be differentiable for back-propagation with simpler tricks than complicated sampling mechanism; 2. the values are better to range in [0,1]; 3. the distribution of values should be two modes, one mode keeps exact zeros while others stay away from zeros obviously. Also, we do not want to construct an expectation-based or sampling-based resource calculation, which can not represent the ac-

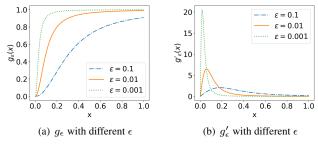


Figure 3. The graph of  $g_{\epsilon}$  (left) and  $g'_{\epsilon}$  (right). As the  $\epsilon$  approaching to zero,  $g_{\epsilon}$  becomes polarized, and  $g'_{\epsilon}(x)$  is zero only at the points where  $g_{\epsilon}(x)$  get exact zeros or near ones, otherwise infinity.

curate resource of a specific sub-net. [66] constructs a heuristic bi-modal regularizer to satisfy above conditions, while we choose the smoothed L0 formulation from principled optimization methods [75]:

$$g_{\epsilon}(x) = \frac{x^2}{x^2 + \epsilon} \tag{2}$$

where  $\epsilon$  is a small positive number. This function has the following important property:

$$g_{\epsilon}(x) \begin{cases} = 0, & x = 0 \\ \approx 1, & x \neq 0 \end{cases}$$
 (3)

The derivative of  $g_{\epsilon}(x)$  w.r.t x is

$$g_{\epsilon}'(x) = \frac{2x\epsilon}{(x^2 + \epsilon)^2} \tag{4}$$

The graph of  $g_{\epsilon}(x)$  and  $g'_{\epsilon}(x)$  are shown in Figure 3.

We can observe that when  $\epsilon$  is small enough,  $g'_{\epsilon}(x)$  approaches to zero only at the points where  $g_{\epsilon}(x)$  get exact zeros or ones, otherwise infinity. It means that the function can only stay stable at 0 or 1. Different from Gumbel-Softmax with decreasing temperature, Function (2) can be exactly zero even when  $\epsilon$  is not small. This good quality implies to us that we can regard  $g_{\epsilon}(x)$  as gate, plugging into the neural network we want to compress. The subscript  $\epsilon$  will be omitted in the following text when not causing misunderstanding. Theoretically, x here can either be an newly introduced trainable parameters, or can directly be the norm of convolution kernel weights. We will analyze both settings in ablation study. We choose the former setting in our algorithm, since it works better than latter one.

During training,  $\epsilon$  gradually decays by a factor smaller than one, and all the gates  $g^{(i)}, i=1,2,...,c$  vary smoothly between 0 and 1. When the training terminates,  $\epsilon$  becomes small enough, and some gates become exact zeros while the others approach ones. Then, we can prune network explicitly by removing the channels corresponding to zero-gates and absorbing other close-to-ones gates into convolu-

tion kernel W. In almost all of our experiments, the performance of the pruned-net is exactly same as that of the super-net before explicitly pruning. In section 4, we will show that the explicitly pruned sub-nets without fine-tuning, already keep pace with or even better than previous state-of-the-art works. We think that the performance consistency between pruned-net and super-net is a good property to help us choose good candidate sub-nets.

## 3.3. Resource-constrained Pruning Formulation

Some works [14, 87] calculate the resource by expectation, which do not represent the accurate resource of a specific sub-net. Instead, we formulate the resource by a bilinear function with respect to the number of channels of each layer [77]. Specifically, if the resource is FLOPs, a bilinear function can strictly model it for most common CNNs. In this paper, we directly take FLOPs as resource to illustrate our pruning algorithm. The FLOPs of a CNN can be accurately formulated as:

$$\mathcal{R}(c) = \sum_{l,k,l \neq k} a_{lk} c_l c_k + \sum_l b_l c_l \tag{5}$$

Where  $c_l$  is the number of channels in the l-th layer,  $a_{lk},b_l\in[0,+\infty)$  is the coefficients of bilinear function. Specifically, for the l-th layer with  $c_l$  channels, gates  $\bar{g}_l(\alpha_l)=\{g_l^{(1)}(\alpha_l^{(1)}),...,g_l^{(c_l)}(\alpha_l^{(c_l)})\}$  with introduced learnable parameters  $\bar{\alpha}_l=\{\alpha_l^{(1)},...,\alpha_l^{(c_l)}\}$  are inserted immediately before the l-th convolution layer. Then, according to Function (3), the resource function (5) can be rewritten as:

$$\mathcal{R}(\alpha) = \sum_{l,k,l \neq k} a_{lk} \|\bar{\alpha}_l\|_0 \|\bar{\alpha}_k\|_0 + \sum_l b_l \|\bar{\alpha}_l\|_0$$
 (6)

where  $\|\cdot\|_0$  is  $\ell_0$ -norm of a vector. The final objective function can be formulated as below:

$$\min_{w,\alpha} \mathcal{F}(w;\alpha) = \mathcal{L}(w;g(\alpha)) + \lambda \mathcal{R}(\alpha) \tag{7}$$

where  $\mathcal{L}$  is the original training loss function, w denotes all the learnable weights of original network,  $\alpha = \{\bar{\alpha}_1,...,\bar{\alpha}_L\}$  denotes all the introduced learnable parameters for gates, L is the total number of layers, and  $\lambda$  is a balance factor.

## 3.4. Optimization via Alternative Relaxation

Objective function (7) is not easy to solve due to the existence of  $\ell_0$ -norm. w and  $\alpha$  in  $\mathcal{L}(w;g(\alpha))$  can be updated by common gradient descent algorithm, just as did in normal training of baseline model. For  $\mathcal{R}(\alpha)$ , we can use proximal-SGD [57] to update  $\alpha$ . The proximal operator  $prox_{\eta_1\lambda\mathcal{R}(\alpha)}(\hat{\alpha})$  is defined as:

$$\underset{\alpha}{\arg\min} \frac{1}{2} \|\alpha - \hat{\alpha}\|^2 + \eta_1 \lambda \mathcal{R}(\alpha) \tag{8}$$

Formulation (8) is a proximal problem with a bi-linear  $\ell_0$ -norm format. We solve it iteratively by alternative relaxation. Specifically, we alternately update  $\bar{\alpha}_l$  by relaxing  $\|\bar{\alpha}_l\|_0$  to  $\|\bar{\alpha}_l\|_1$  while treating other  $\bar{\alpha}_k, k \neq l$  as constants at current time, and then exchange the variables and constants roles to perform relaxing operation again. The details are shown in Algorithm 1. Note that in line 4, we do not relax  $\|\bar{\alpha}_k\|_0$  to  $\|\bar{\alpha}_k\|_1$  when it is treated as constant.

**Algorithm 1:** Alternative relaxation to solve (8) iteratively.

```
Input: learning rates \eta_1, balance factor \lambda, predefined iteration number N, and \hat{\alpha}.

Result: \alpha^*

1 Initialize t=1, \alpha^1 (initial values);

2 while t \leq N do

3 | for l=1 \rightarrow L do

4 | \alpha_l^{t+1} = \arg\min_{\bar{\alpha}_l} \frac{1}{2} \|\bar{\alpha}_l - \hat{\alpha}_l\|^2 + \eta_1 \lambda \sum_{k,k \neq l} a_{lk} \|\bar{\alpha}_k^t\|_0 \|\bar{\alpha}_l\|_1 + b_l \|\bar{\alpha}_l\|_1

5 | end

6 | t=t+1

7 end

8 \alpha^* = \alpha^N
```

Line 4 in Algorithm 1 is proximal  $\ell_1$ -norm problem, which has closed form solution:

$$\alpha_{l,i}^{t+1} = \begin{cases} \hat{\alpha}_{l,i} - \beta, & \hat{\alpha}_{l,i} \ge \beta \\ 0, & -\beta < \hat{\alpha}_{l,i} < \beta \\ \hat{\alpha}_{l,i} + \beta, & \hat{\alpha}_{l,i} \le -\beta \end{cases}$$
(9)

where  $\beta = \eta_1 \lambda \sum_{k,k \neq l} a_{lk} \|\bar{\alpha}_k^t\|_0 + b_l$ , and the subscript i means the i-th entry of the vector.

In fact, general proximal  $\ell_0$ -norm problem also has closed form solution. The reason we choose to relax  $\ell_0$ -norm to  $\ell_1$ -norm in our algorithm is that all entries of  $\alpha$  in proximal  $\ell_1$ -norm operation can shrink a small step at each iteration, which may compete with the change caused by  $\mathcal{L}(w;g(\alpha))$ . This effect exists regardless of the initialization value of  $\alpha$ . However, for proximal  $\ell_0$ -norm operation, the entries with large initialization values will always fluctuate in a small range, while the other entries initialized with small values quickly vanish at the first few iterations and very hard to recover later by  $\mathcal{L}(w;g(\alpha))$ . We find in our experiments that proximal  $\ell_1$ -norm operation works robustly while proximal  $\ell_0$ -norm operation is very sensitive to initialization of  $\alpha$ .

As for convergence of Algorithm 1, with large number of simulations, we find that it always converges quickly, although it's not necessarily the globally optimal solution. An simple illustration example is shown in Figure 4, which converge quickly with very few iterations. In all experiments in this paper, we find iteration N=1 works well enough.

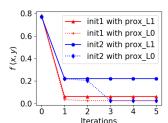


Figure 4: A simple example to show the convergence of Algorithm 1. The objective function here is set to be  $f(x,y) = \frac{1}{2}((x-u)^2 + (y-v)^2) + c||x||_0||y||_0$ , where  $u = \{0.1, 0.2\}, v = \{-0.3, 0.5, 0.6\}$  and c = 0.01. init1 denotes the initialization

with  $x = \{0.1, 0.8\}$  and  $y = \{0.7, 0.3, 0.8\}$ , while init2 denotes the initialization with  $x = \{0.4, 0.1\}$  and  $y = \{0.7, 0.5, 0.0\}$ .

# 4. Experiments

We conduct classification experiments on CIFAR-10 [36] dataset with ResNet-56, VGG-16 and MobileNet-V2, and ImageNet ILSVRC-12 dataset [9] with MobileNet-V1/V2, ResNet-50 respectively. Our main experiments are done by introducing learnable parameters  $\alpha$  while we also do ablation study with using convolution kernel as parameters for gate function in Equation (2). We compare GDP with previous works: EagleE [40], PFS [72], MetaP [49], AMC [23], NetAdapt [79], DCP [89], GFS [81], LeGR [7], DMCP [18], SCP [34], SLRTD [60], OICSR [42], Taylor [56], CCP [59], GD [82], Hinge [43], CURL [54], HRank [44], LFPC [22], C-SGD [10], SCOP [69], AutoSlim [83], ABCpruner [45], DMC [14], VCNNP [86]. It should be pointed out that AutoSlim [83] uses totally different training settings from most works, and prunes from more heavier models, thus not comparable. Also we should mention that MDP [17] is more likely NAS method for searching sub-nets; EagleE [40] uses adaptive BN strategy to improve various sub-nets evaluation. We did not compare with these special papers on CIFAR-10, due to their extremely good performance reported, with a large gap to other papers. Although aforementioned two papers are not fair to compare with our pruning method, our simple yet effective method still achieves better performance than these out-of-scope papers on ImageNet with various benchmark networks. The drawing scripts containing all numerical values of all figures are left in supplementary materials.

#### 4.1. Implementation Details

 $\alpha$  and  $\epsilon$  settings For all experiments, the  $\alpha$  and  $\epsilon$  are initialized with 1.0 and 0.1 respectively. The learning rate of  $\alpha$  is one tenth of that of the other network parameters for classification task and there is no weight decay on  $\alpha$  when training. For CIFAR-10, we train 350 epochs with decaying  $\epsilon$  by 0.96 every epoch, while for ImageNet, we train 140 epochs with decaying  $\epsilon$  by 0.9 every epoch. For DeepLabV3Plus-ResNet50, we train it for 60k iterations, and decay  $\epsilon$  by 0.97 every 120 iterations. Also, the initial learning rate of  $\alpha$  is 9e-4 as it is different for classifier and backbone. We smooth the shrinkage of  $\alpha$  when necessary.

	Method	Baseline(%)	Ratio(%)	Abs. Acc.	$\Delta$ Acc.
VGG-16	PFS	93.44	50	93.63	0.19
	SCP	93.85	33.77	93.79	-0.06
	VCNNP	93.25	39.1	93.18	-0.07
	Hinge	94.02	60.93	93.59	-0.43
	HRank	93.96	46.4	93.43	-0.53
	GDP (Ours)	93.89	30.55	93.99	0.1
ResNet-56	PFS	93.23	50	93.05	-0.18
	ABCPruner	93.26	45.87	93.23	-0.03
	HRank	93.26	50	93.17	-0.09
	SCOP	93.7	44	93.64	-0.06
	LFPC	93.59	52.9	93.72	0.13
	LeGR	93.9	47	93.7	-0.2
	GDP (Ours)	93.9	34.36	93.55	-0.35
	GDP (Ours)	93.9	46.65	93.97	0.07
Mobile-V2	SCOP	94.48	59.7	94.24	-0.24
	MDP	95.02	71.29	95.14	0.12
	DMC	94.23	60	94.49	0.26
	GDP (Ours)	94.89	53.78	95.15	0.26

Table 2. CIFAR-10. Different methods' accuracy (Abs. Acc.) and their differences from corresponding baselines ( $\Delta$  Acc.) are reported for a full comparison. Note that the structure of MobileNet-V2 for CIFAR-10 may differ in different works.

CIFAR-10 Experiments We randomly flipped horizontally, randomly cropped size of 32 with padding 4 on images, and perform normalization to augment the data when training. Table 2 shows the performance of CIFAR-10 on various network structure. The network structure for VGG-16 and ResNet-56 is same as [44]. MobileNet-V2 is from LeGR [7], but we do not find the CIFAR-10 results in LeGR. Note that the structure of MobileNet-V2 for CIFAR-10 may differ in different works. We train it with cosine learning rate [50] without label smoothing [67]. These experiments are conducted on single NVIDIA 2080Ti GPU.

**ImageNet Experiments** We conduct experiments on ImageNet ILSVRC-12 dataset [37] with MobileNet-V1/V2 [26, 64] and ResNet-50 [21]. We use this dataset the same way as PyTorch official example<sup>1</sup>.

For MobileNet-V1, the initial learning rate is set to 0.1 and decayed by 0.1 every 35 epochs for training. No label smoothing is used, same to most previous works. Figure 5 shows the performance of GDP compared to other works.

MobileNet-V2 is designed with inverted residual blocks, and it is more efficient than MobileNet-V1, which is more challenging for model pruning. Same as most previous works, we use label smoothing and cosine learning rate to boost performance. Figure 6 shows the performance of GDP compared with other works. We can clearly see that GDP has enjoyed great advantages over previous works. We do not show AutoSlim [83] here because it prune

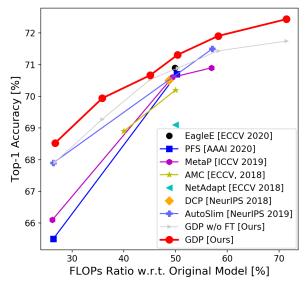


Figure 5. Performance of GDP compared with other works with Top-1 Accuracy of ImageNet on MobileNet-V1. We can see the great advantages of GDP (even without fine-tuning), especially at low FLOPs ratio. Our baseline is 71.314. Note that our results are even better than AutoSlim [83].

MobileNet-V2 from a more heavier super-net (MobileNet-V2  $1.5\times$ ) and used many training tricks. We do not use this settings to keep comparable with most others works.

ResNet-50 is a network with much more FLOPs. Some works use cosine learning rate and/or label smoothing while others not, the baseline also varies dramatically. So we plot two version curves, one with cosine learning rate and label smoothing, the other neither. See Figure 7, GDP is also superior than other works. Experiments for MobileNet-V1/V2 are conducted on two NVIDIA 2080Ti GPUs, while experiments for ResNet-50 are on two NVIDIA V100 GPUs.

Pascal VOC with DeepLabv3+ DeepLabv3+ [6] is designed with a decoder module besides the Atrous Spatial Pyramid Pooling(ASPP) to extend DeepLabv3 [5] and get better segmentation results. It contains a more complex network structure than classification tasks, so we choose it to demonstrate the generalization and robustness of GDP. We use ResNet-50 as backbone, and follow the same training settings as public implementation <sup>2</sup>. The output stride (OS) for training and validation is 16. Table 3 shows the mIoU of Pascal VOC segmentation task, and several visual results are shown in supplementary materials. For the sub-net with 38.4% FLOPs, we are surprised to find that the branch with atrous-rate 18 in the ASPP module is totally pruned, the branch with atrous-rate 12 only 6 channels left, however, gaining slightly better performance.

Furthermore, we also deploy GDP method in Style

<sup>&</sup>lt;sup>1</sup>https://github.com/pytorch/examples/blob/master/imagenet/main.py

<sup>&</sup>lt;sup>2</sup>https://github.com/VainF/DeepLabV3Plus-Pytorch

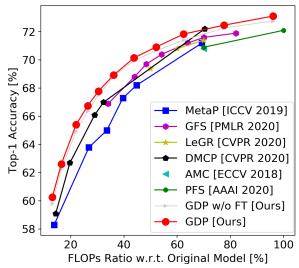


Figure 6. Performance of GDP compared with other works with Top-1 accuracy of ImageNet on MobileNet-V2. Our baseline is 72.13, similar to most previous works. We can see the great advantages of GDP (even without fine-tuning), especially at the same FLOPs ratios less than 40%, the gap of top-1 accuracy is close to two percentages (it is better to compare dots strictly vertically).

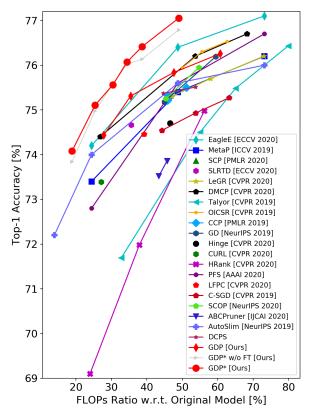


Figure 7. Top-1 Accuracy of Imagenet on ResNet-50. Different training settings are used for different works, and the baseline also varies greatly. We provide two version curves here. GDP\* means training with both cosine learning rate and label smoothing (baseline 77.414), while GDP use step learning rate without label smoothing (baseline 76.140).

FLOPs(%)	38.4	23.3	12.1	5.2	Baseline
mIOU	0.779	0.767	0.721	0.666	0.774

Table 3. The mIoU on Pascal VOC 2012 validation set with DeepLabv3+ using ResNet-50 as backbone. FLOPs here means the remaining FLOPs ratio of the sub-net. We can see that GDP can obtain a sub-net containing only 38.4% FLOPs without performance drop. More visual results are in supplementary materials.

Transfer task, based on official PyTorch implementation of [33], achieving great performance on pruned models with large portion of FLOPs savings. Visual results of pruned models are shown in supplementary materials.

#### 4.2. Discussion

**Polarization** Figure 9 shows density distributions of gate values during the training process. We can see that the density distributions of gates change smoothly and the number of zero-gates becomes stable after epoch 70. The polarization depends on the value of  $\epsilon$ . A larger  $\epsilon$  results in a smoother gate with less polarization; a smaller  $\epsilon$  makes (2) closer to  $L_0$  norm but with less numerical stability. An ablation study for the effects of  $\epsilon$  is attached in supplementary materials. Our method is robust to different values of  $\epsilon$ .

**Consistency** We do not need to sample sub-net or calculate expectation when training. Figure 11 shows the training stability of GDP. When training terminates, there is only one candidate sub-net left. Different value of  $\lambda$  in Function (7) result in sub-net with different FLOPs and performance. We want the performance of the super-net to reflect the performance of the final sub-net as much as possible. We set multiple hyper-parameters to run the GDP compression training. Figure 10 reflects the relationship between performance of super-net before explicitly pruning and the sub-net after fine-tuning.

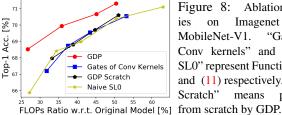


Figure 8: Ablation studon Imagenet MobileNet-V1. "Gates of Conv kernels" and "Naive SL0" represent Function (10) and (11) respectively. "GDP Scratch" means pruning

## 4.3. Ablation studies

**Not introducing**  $\alpha$  There is an alternative solution that instead of introducing the learnable parameters  $\alpha$ , we can also use the existing convolution kernels weights as x in Equation (2). The objective function is then becomes:

$$\min_{w} \mathcal{F}(w) = \mathcal{L}(w; g(w)) + \lambda \mathcal{R}(w)$$
 (10)

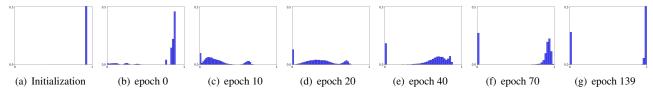


Figure 9. The density histogram of gates as the training goes on. All the X-axis represents the gate values in [0,1], Y-axis represents the frequency of gates (we truncate it to 0.5 for better visualization). At beginning, the  $\alpha$  and  $\epsilon$  are initialized 1.0 and 0.1 respectively, so all the gates are  $\frac{1}{1.1} \approx 0.909$  (see Function (2)) at the beginning and gates are distributed smoothly between 0 and 1. After some epochs, the gates becomes more and more polarized. Finally, some gates become exact zeros while others close to ones. The figure is based on experiment conducted on Imagenet with MobileNet-V2, the final remaining FLOPs ratio is 0.45.

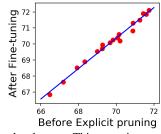


Figure 10: The relationship between Top-1 accuracy of super-net before explicitly pruning and that of the corresponding sub-net after finetuning. The correlation coefficient is 0.993. We can see that the accuracy of super-net effectively reflects that of the fi-

nal sub-net. This experiments are conducted on ImageNet with MobileNet-V1.

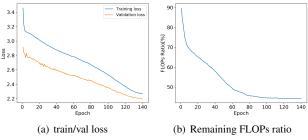


Figure 11. The stability of GDP. We can clearly see that the training/validation loss, and the remaining FLOPs ratio is changing stably as training goes on. This figure is plotted on ImageNet with MobileNet-V2. As we decay  $\epsilon$  in Function (2) after each epoch, the BN parameters are re-calculated for plotting validation loss. All the loss is averaged within each epoch, while the FLOPs ratio is calculated at the end of each epoch.

For the l-th layer convolution kernel  $W_l \in \mathbb{R}^{c \times d \times r \times r}$ , we can let  $x_l^{(i)} = \|W_{l[:,i,:,:]}\|_2$  where i=1,2,...,d, is the indication of input channels, and substitute  $x_l$  defined here for  $\alpha$  in Equation (6). In this case, we initialize  $\epsilon_l^{(i)}$  in every gate to one-tenth of the corresponding  $\|W_{l[:,i,:,:]}\|_2$ , so the initial value of all gates is  $\frac{1}{1.1} \approx 0.909$ , the same to when  $\alpha$  is introduced. The performance is plotted in Figure 8, labeled with "Gates of Conv Kernels". We guess that the relatively poor performance of using  $\|W_{l[:,i,:,:]}\|_2$  may be caused by dual roles of both feature extraction and gate controlling, which has the opposite thoughts from [63, 73], or caused by the occasional instability of the norm of w when  $\epsilon$  is small enough.

g as regularization g(x) was originally used as regularization term added to the original objective function of smooth L0 optimization [75]. Follow this idea, we can also naively define our objective function as bellow:

$$\min_{w} \mathcal{F}(w) = \mathcal{L}(w) + \lambda \mathcal{R}(g(w)) \tag{11}$$

The reason we did not take this approach is similar to above. The norm of w is occasionally unstable when training, maybe caused by g(w). However, if we introduce  $\alpha$  as in Equation (7), it is not  $\alpha$  itself but  $g(\alpha)$  to participate in the network forward.  $g(\alpha)$  is stably taking values in range [0,1]. The performance is shown in Figure 8, labeled with "Naive SL0".

**Pruning from Scratch** Figure 8 also shows some results by pruning from scratch (labeled with "GDP Scratch") rather than from a well-trained model. With the same hyperparameters, the pruning speed of "GDP Scratch" became particularly faster and a more compressed sub-net was obtained. This relatively poor performance might result from that some important channels have no resistance to being pruned before they get learned well enough.

## 5. Conclusion

In this paper, we proposed a novel differentiable polarized gate module in network pruning task and obtain state-of-the-arts performance on various benchmark experiments. Comparing with other optimization-based pruning methods, the advantages of our method are five folds: simpler to implementation; fewer hyper-parameters to tune; obtain good sub-nets easily; take less time; not rely on any specific network structure. The disadvantage is that the model pruning ratio cannot be specified in advance, which now need to manually adjust the value of  $\lambda$  to balance the accuracy and resource consumption of network. We leave this problem in future works. The gates with differentiable polarization proposed in this paper can also be easily extended into neural architecture search (NAS) tasks.

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