A. Outlier-Aware Loss

A.1. Analysis of Outlier-Aware Loss

We produced some histograms of the pixel-to-pixel difference values obtained by various models on several datasets, and find that often the difference values concentrate on the mean that is close to zero and show a similar frequency curve as some bell-shaped distribution with a small variance. Thus, we postulate that the true value is equal to the predicted value plus error as \( y = \hat{y} + \delta \), in which \( \delta \) follows an unknown distribution with a mean of 0, which is not always the case and influenced by the data distribution, loss function, and the model itself. Sometimes, the results generated by some models may have a very significant bias. But in this case, with a similar frequency curve as the Laplacian distribution due to the data distribution, loss function, and the model itself. Sometimes, the results generated by some models may have a very

A.2. Generalized Outlier-Aware Loss

Apart from image restoration problems where \( L_1 \) loss is mostly utilised, the Outlier-Aware Loss \( L_{OA} \) could be further generalized by introducing a norm parameter \( p \) and changing the definition of scale parameter \( b \) for the consistency between MLE(maximum likelihood estimation) and minimizing the loss function. For example, for problems where \( L_2 \) loss outperforms \( L_1 \) loss, we could set \( p \) to be 2 and define \( b \) to be \( 2\sigma^2 \) for the consistency between minimizing \( L_2 \) loss and MLE on Gaussian distribution.

\[
L_{OA} = \frac{1}{HW} \sum_{i=0}^{H-1} \sum_{j=0}^{W-1} \left[ |\delta_{i,j}|^p \times \left( 1 - e^{-\alpha|\delta_{i,j} - \mu|/b} \right) \right] 
\]
(a) **(left)** The weight $W(x|\mu, b) = 1 - \exp(-\alpha|x - \mu|/b)$ for adjusting the original loss over the difference between the network output and ground truth $x = \Delta = I(\text{Output}) - I(\text{GT})$ as the tunable hyperparameter $\alpha$ varies. For simplicity, it is assumed that the mean $\mu$ and the scale parameter $b$ of $\Delta$ are 0 and 1 respectively. It shows that larger $\alpha$ makes $L_{OA}$ approach $L_1$, while smaller $\alpha$ reduces the weights of the loss by well predicted pixels to a larger degree. **(right)** The loss value over $\Delta$: compared with $L_2$, $L_{OA}$ reduces the weights of the well predicted pixels instead of squaring the loss values of badly predicted pixels. Since $L_{OA} \leq L_1$, $L_1$ becomes the asymptote of $L_{OA}$ as $\alpha$ becomes larger.

(b) **(left)** The normalized loss density that we define as loss value times the probability density of that loss value $L \times p(L)$, which leads to the expectation of loss to be $1$ as $\int L \times p(L) dL = 1$. We could understand the loss density as the contribution to the total loss by this loss value. It could be observed that $L_{OA}$ reshapes the loss density by increasing the proportion of outliers and reducing the proportion of well predicted pixels in the total loss. Specifically, a smaller $\alpha$ causes a larger proportion transfer towards the outliers. Therefore, we successfully increase the importance of badly predicted outliers and decrease the importance of well predicted pixels in the training. Initially, we try to collect the loss values by testing the pre-trained model but found the vibration of the values cause the curve to be too messy. Thus we decide to randomly generate pixel-to-pixel loss values by sampling from the Laplacian distribution since the loss values collected from the test look very close to it. Note that this is not always the case due to the variation of the dataset and network itself. **(right)** The cumulative normalized loss, which is the integration of normalized loss density, shows the growth of total loss when we integrate the total loss from small loss values to large ones. It is observed that for $L_1$ loss, the loss values smaller than 3 forms 80% of the total loss, but for $L_{OA}$ loss with $\alpha = 0.1$, the loss values smaller than 3 only forms 30% of the total loss.

Figure 8: Analysis of the Outlier-Aware Loss: Combining (a) and (b) could demonstrate the function of $L_{OA}$ and $\alpha$. It could be seen from all the four figures that larger $\alpha$ make $L_{OA}$ approach $L_1$ but would not exceed $L_1$. So that, we could avoid the regression to the mean problem by $L_2$. 
As mentioned above, different loss functions imply the MLE of different data distributions. And hence, a variety of loss functions should be proposed and studied to respond to the various data distributions in the real world.

B. ISP unsupervised warming up strategy

Warming up strategy used in deep learning usually aims at overcoming optimization challenges early in training [25]. In our ISP task, besides this aim, we want our network to have the ability to recover the defective pixel, which often occurs in ISP raw data because of the physical issues on the camera sensor, by their neighbor pixels. Inspired by [32], we randomly mask $\frac{3}{6}$ of the input by $3 \times 3$ mask tokens and train the network to recover them with $1e - 6$ learning rate for 10 epochs.

C. Re-parameterizing the ConvRep block

The input feature and output feature of ConvRep block in SYENet shall have $C_{\text{in}}$ and $C_{\text{out}}$ channels. ConvRep block has $N$ branches, each of which can be designed specifically. Each branch could convert the channel of the input feature to $R \times C_{\text{out}}$, where $R$ is the hyperparameter. The features by $N$ branches shall be concatenated in channel dimension, and hence the number of channels becomes $N \times R \times C_{\text{out}}$. Then the final $1 \times 1$ convolution converts the feature with $N \times R \times C_{\text{out}}$ channels to output feature with $C_{\text{out}}$ channels. The reparameterization shall turn the complex ConvRep block back to a normal convolution block for inference.

The convolutional block injected by an input feature tensor $I^{(\text{in})} \in \mathbb{R}^{N \times C_{\text{in}}} \times W \times H$ shall have a weight matrix $W \in \mathbb{R}^{C_{\text{out}}} \times C_{\text{in}} \times k_W \times k_H$ and a bias matrix $B \in \mathbb{R}^{C_{\text{out}}}$. In SYENet, it is set that all the convolutions generate the output with the same shape of the input feature map by arranging the padding carefully for the purpose of concatenation afterward. Batch normalization could be expressed as Eq. 13 converting input $X$ to $Y$, in which $\gamma$ and $\beta$ are normally assigned to be 1 and 0 respectively for no linear transformation.

$$Y = \frac{X - \mathbb{E}[X]}{\sqrt{\text{VAR}[X] + \epsilon}} \times \gamma + \beta$$

$$= \left( \frac{X}{\sqrt{\text{VAR}[X] + \epsilon}} + \frac{-\mathbb{E}[X]}{\sqrt{\text{VAR}[X] + \epsilon}} \right) \times \gamma + \beta$$

$$= (W^{(\text{bn})} \times X + B^{(\text{bn})}) \times \gamma + \beta$$

It is obvious that the batch normalization towards the output of the convolution layer could be converted to the batch normalization towards the weight and bias of the convolution layer, as shown in Eq. 15. In addition, the concatenation of convolution output could also be re-parameterized as the concatenation of the weights and bias of all the convolution layers, as shown by Eq. 17. In the following equations, $\bullet$ means convolution operation. It is assumed the convolution in each branch has $C_{\text{out}}$ output channels, and hence the concatenation of convolutions from all the branches can be reparameterized as a convolution with $N \times C_{\text{out}}$ output channels.

$$F^{(\text{conv})} = \text{CONV}(F^{(\text{in})}) = F^{(\text{in})} \bullet W^{(c)} + B^{(c)}$$

(14)

Substituting Eq. 14 into the Eq. 13 could obtain the complete expression of convolution followed by batch normalization, which is shown

$$F^{(\text{conv} + \text{bn})} = \text{BN}(F^{(\text{conv})})$$

$$= (W^{(\text{bn})} \times F^{(\text{conv})} + B^{(\text{bn})}) \times \gamma + \beta$$

$$= \left[ W^{(\text{bn})} (F^{(\text{in})} \bullet W^{(c)} + B^{(c)}) + B^{(\text{bn})} \right] \times \gamma + \beta$$

$$= (F^{(\text{in})} \bullet W^{(\text{bn})} W^{(c)} + W^{(\text{bn})} B^{(c)} + B^{(\text{bn})}) \times \gamma + \beta$$

$$= F^{(\text{in})} \bullet \gamma W^{(\text{bn})} W^{(c)} + \gamma (W^{(\text{bn})} B^{(c)} + B^{(\text{bn})}) + \beta$$

$$= F^{(\text{in})} \bullet (W^{(c + \text{bn})} + B^{(c + \text{bn})})$$

(15)
\[ F^{(\text{cat})} = \text{CAT}(F^{(\text{conv})}, F^{(\text{conv+bn})}) \]
\[ = F^{(\text{in})} \cdot \text{CAT}(W^{(c+bn)}) + \text{CAT}(B^{(c+bn)}) \]
\[ = F^{(\text{in})} \cdot W^{(\text{cat})} + B^{(\text{cat})} \] (16)

The numbers of \( F^{(\text{conv})} \) and \( F^{(\text{conv+bn})} \) to be concatenated may vary according to the specific implementation and could be 0 if necessary.

\[ F^{(\text{rep})} = \text{CONV}_{1 \times 1}(F^{(\text{cat})}) \]
\[ = (F^{(\text{in})} \cdot W^{(\text{cat})} + B^{(\text{cat})}) \cdot W^{(c)} + B^{(c)} \]
\[ = F^{(\text{in})} \cdot \text{MATMUL}(W^{(\text{cat})}, W^{(c)}) + \text{MATMUL}(B^{(\text{cat})}, W^{(c)}) + B^{(c)} \]
\[ = F^{(\text{in})} \cdot W^{(\text{rep})} + B^{(\text{rep})} \] (17)

\( \text{MATMUL} \) means matrix multiplication. \( W^{(c)} \) and \( W^{(c+bn)} \) both have the shape of \( C^{(\text{out})} \times C^{(\text{in})} \times K_h \times K_w \), while \( W^{(c+cat)} \) after concatenation has the shape of \( \text{NRC}^{(\text{out})} \times C^{(\text{IN})} \times K_h \times K_w \) where \( N \) is the number of branches and \( K_h \) and \( K_w \) are the kernel size. As the input feature and output feature of this ConvRep block are designed to have \( C^{(\text{in})} \) and \( C^{(\text{out})} \) channels, the final \( \text{CONV}_{1 \times 1} \) after concatenation should have weight matrix with the shape of \( C^{(\text{in})} \times \text{NRC}^{(\text{out})} \times 1 \times 1 \). By squeezing, permuting, and matrix multiplication operations, the reparameterized weight matrix \( W^{(\text{rep})} \) and bias matrix \( B^{(\text{rep})} \) could be obtained as shown by Eq. 17.

**D. Two-branch feature fusion**

After the re-parameterization, the two-branch structure could be converted back to two parallel convolution blocks and later on be fused by a multiplication operation as activation. The output fusion by QCU is represented by Eq. 18, and each branch can be represented as equations below Eq. 19 and Eq. 20 as \( \hat{I} \) and \( \tilde{I} \). \( B \in \mathbb{R}^{N \times C \times 1 \times 1} \) is the learnable bias in QCU.

\[ I = \hat{I} \times \tilde{I} + B \] (18)

For the convolution with kernel size of \( (k_m, k_n) \), \( k_m^{(h)} \) and \( k_n^{(h)} \) are defined as \( \frac{1}{2}(k_m - 1) \) and \( \frac{1}{2}(k_n - 1) \). \( l \) is the index of channels for input and output, while \( L \) represents the total number of channels.

\[ \hat{I}_{i,j}^{l_{\text{out}}} = \hat{I}_{i,j}^{l_{\text{out}}} + \sum_{l_{\text{in}}=0}^{L_{\text{in}}} \sum_{m=-K_m^{(h)}}^{K_m^{(h)}} \sum_{n=-K_n^{(h)}}^{K_n^{(h)}} (\hat{I}_{i,l_{\text{in}},j,n}^{l_{\text{in}}} \times K_{m,n}^{l_{\text{out}},l_{\text{in}}}) \] (19)

\[ \tilde{I}_{i,j}^{l_{\text{out}}} = \tilde{I}_{i,j}^{l_{\text{out}}} + \sum_{l_{\text{in}}=0}^{L_{\text{in}}} \sum_{m=-K_m^{(h)}}^{K_m^{(h)}} \sum_{n=-K_n^{(h)}}^{K_n^{(h)}} (\tilde{I}_{i,l_{\text{in}},j,n}^{l_{\text{in}}} \times K_{m,n}^{l_{\text{out}},l_{\text{in}}}) \] (20)

So that, the output feature value for a certain output channel in a fixed position \((i, j)\) can be represented precisely as Eq. 21 which could be further expressed as Eq. 22.

\[ \hat{I}_{i,j}^{l_{\text{out}}} = \hat{I}_{i,j}^{l_{\text{out}}} \times \hat{I}_{i,j}^{l_{\text{out}}} + b_{i,j} \] (21)

\[ \tilde{I}_{i,j}^{l_{\text{out}}} = \tilde{I}_{i,j}^{l_{\text{out}}} \times \tilde{I}_{i,j}^{l_{\text{out}}} + b_{i,j} \]

\[ \hat{I}_{i,j}^{l_{\text{out}}} = \hat{I}_{i,j}^{l_{\text{out}}} \times \hat{I}_{i,j}^{l_{\text{out}}} + \sum_{l_{\text{in}}=0}^{L_{\text{in}}} \sum_{m=-K_m^{(h)}}^{K_m^{(h)}} \sum_{n=-K_n^{(h)}}^{K_n^{(h)}} (\hat{I}_{i,l_{\text{in}},j,n}^{l_{\text{in}}} \times K_{m,n}^{l_{\text{out}},l_{\text{in}}}) + \sum_{l_{\text{in}}=0}^{L_{\text{in}}} \sum_{m=-K_m^{(h)}}^{K_m^{(h)}} \sum_{n=-K_n^{(h)}}^{K_n^{(h)}} (\tilde{I}_{i,l_{\text{in}},j,n}^{l_{\text{in}}} \times K_{m,n}^{l_{\text{out}},l_{\text{in}}}) \] (22)
Consequently, the general formulation of the output feature expression obtained by the re-parameterized two-branch structure shall be \( B + (K + \hat{K})I + KI^2 \) which is in quadratic form rather than the linear form of \( B + KI \). So, the capability of feature representation by this structure could be enhanced.

However, the expression that could be converted into the form of \((\hat{K}I + \hat{B}) \times (\hat{K}I + \hat{B})\) have a strict constraint in feature representation pattern, which means the representation must have two sets of fixed values in solution space that are \((-\hat{I}/\hat{B}, 0)\) and \((-\hat{I}/\hat{B}, 0)\). Hence in order to solve this issue, an additional learnable bias \( B \) is merged into the network to boost the ability to learn features.

### E. Theoretical analysis about different fusion methods on a toy example

As mentioned in the paper, we compared different fusion methods including ADD (element-wise addition), CAT+CONV (concatenation plus convolution), MUL (element-wise multiplication), and the proposed QCU (Quadratic Connection Unit). As presented by Fig. 4 and Table 4, QCU gains higher PSNR than these techniques. It could be roughly explained by that the mathematical expression of QCU shows more powerful representative capability than other fusion methods.

The analysis is conducted on a toy example, which is a simple two-branch network with input tensor \( X \) of shape \( N \times C \times H \times W \), output tensor \( Y \), and two convolutions in each branch CONV\(^{(c)}\) and CONV\(^{(s)}\). Assuming the input channels and output channels are all equal to \( C \), and consequently, we have the weights and biases of the two convolutions, which are \( W^{(c)} \), \( B^{(c)} \), \( W^{(s)} \) and \( B^{(s)} \), all with \( C \) channels. For CAT+CONV method, we have one extra convolution CONV\(^{(cat)}\) with input channel \( 2C \), output channel \( C \), weight \( W^{(cat)} = [W^{(cat)}(c), W^{(cat)}(s)] \) and bias \( B^{(cat)} = [B^{(cat)}, B^{(cats)}] \). \( W^{(cat)} \) has shape \( C \times 2C \times K \times K \), while \( W^{(cat)}(c) \) and \( W^{(cat)}(s) \) have shape \( C \times C \times K \times K \), in which \( K \) is the kernel size.

The model could be further simplified by setting \( C = 1 \), and kernel size to be 1 as well. The channel and kernel assumption could convert the convolution into simple linear transformation.

\[
Y^{(ADD)} = (W^{(c)}X + B^{(c)}) + (W^{(s)}X + B^{(s)}) = (W^{(c)} + W^{(s)})X + (B^{(c)} + B^{(s)})
\]

\[
Y^{(CAT)} = CAT(W^{(c)}X + B^{(c)}, W^{(s)}X + B^{(s)})
\]

\[
\begin{align*}
Y^{(CAT+CONV)} &= W^{(cat)}Y^{(CAT)} + B^{(cat)} \\
&= W^{(cat)}(W^{(c)}X + B^{(c)}) + W^{(cat)}(W^{(s)}X + B^{(s)}) + B^{(cats)} \\
&= (W^{(cat)}W^{(c)} + W^{(cats)}W^{(s)})X + (W^{(cat)}B^{(c)} + B^{(cats)}B^{(s)} + B^{(cats)})
\end{align*}
\]

\[
Y^{(MUL)} = (W^{(c)}X + B^{(c)}) \times (W^{(s)}X + B^{(s)}) = (W^{(c)}W^{(s)}X^2 + (W^{(c)}B^{(s)} + W^{(s)}B^{(c)})X + B^{(c)}B^{(s)})
\]

\[
\begin{align*}
Y^{(QCU)} &= (W^{(c)}X + B^{(c)}) \times (W^{(s)}X + B^{(s)}) + B^{(QCU)} \\
&= W^{(c)}W^{(s)}X^2 + (W^{(c)}B^{(s)} + W^{(s)}B^{(c)})X + (B^{(c)}B^{(s)} + B^{(QCU)})
\end{align*}
\]

Comparing the expressions of all the fusion methods, the representative capability of these methods could be summarized. As indicated by Table 4, the performance on PSNR by the fusion methods is ranked as QCU > MUL > CAT+CONV > ADD. It is observed that QCU and MUL both have an extra second-order term \( X^2 \), which makes QCU and MUL have the more powerful representative capability to fit more complex models. On the other hand, CAT+CONV could be regarded as a weighted addition operation, in which the weights of \( W^{(c)} \), \( W^{(s)} \), \( B^{(c)} \), and \( B^{(s)} \) are all adjusted by the following convolution CONV\(^{(cat)}\). Thus, CAT+CONV fusion could generate more complex fitting. It should be noted that those tricks could significantly improve the performance of small networks. But in large models, since the networks already have very large depth and width to fit complex distributions, those tricks might not be necessarily helpful.
F. Comparisons with models in SR task with scale factor $\times 3$

More comparisons for the scale factor of $\times 3$ are shown in Table 7. We trained these models on DIV2K and tested them on Set5, Set14, BSD100, and Urban100 following Table 1.

<table>
<thead>
<tr>
<th>Method</th>
<th>Scale</th>
<th>#P</th>
<th>Avg latency(ms)</th>
<th>FPS(2K)</th>
<th>Set5</th>
<th>Set14</th>
<th>BSD100</th>
<th>BSD100 Score</th>
<th>Urban100</th>
<th>Urban100 Score</th>
</tr>
</thead>
<tbody>
<tr>
<td>ABPN [20]</td>
<td>$\times 3$</td>
<td>53K</td>
<td>59.3</td>
<td>17</td>
<td>32.99</td>
<td>29.46</td>
<td>28.48</td>
<td>2.264</td>
<td>26.34</td>
<td>2.434</td>
</tr>
<tr>
<td>SCSRN [42]</td>
<td>$\times 3$</td>
<td>67K</td>
<td>43.1</td>
<td>23</td>
<td>33.20</td>
<td>29.57</td>
<td>28.56</td>
<td>3.634</td>
<td>26.52</td>
<td>4.294</td>
</tr>
<tr>
<td>HOPN [42]</td>
<td>$\times 3$</td>
<td>48K</td>
<td>32.1</td>
<td>31</td>
<td>32.66</td>
<td>29.28</td>
<td>28.35</td>
<td>3.647</td>
<td>26.33</td>
<td>2.966</td>
</tr>
<tr>
<td>SYENet (Ours)</td>
<td>$\times 3$</td>
<td>7.9K</td>
<td>11.3</td>
<td>88</td>
<td>32.93</td>
<td>29.44</td>
<td>28.47</td>
<td>12.234</td>
<td>26.33</td>
<td>12.595</td>
</tr>
</tbody>
</table>

Table 7: Comparison on super-resolution issue between the results by PSNR(dB), SSIM, and comprehensive score with SOTA: the models are ranked by score defined by Eq. 9 on BSD100 dataset. The normalization factors $C$ for BSD100 Score are $1.8E16(\times 2)$, $1E14(\times 3)$, and $5E14(\times 4)$. While the normalization factors $C$ for Urban100 Score are $2.5E15(\times 2)$, $3.5E12(\times 3)$, and $1.6E13(\times 4)$.

G. Constraints of low-level vision tasks in mobile devices

G.1. Constraints of input size and output size

Low-level vision tasks are basically regression questions. The output of the model is at least the same size as the input, and particularly for super-resolution problems, the output size shall be many times larger than that of the input. However, the output of high-level vision tasks like classification questions could be a vector containing scores of different object labels, which is much smaller than the low-level vision tasks output. Apart from output size, the input to high-level vision tasks could also be significantly smaller than low-level vision task input. Normally, the input image would be resized into as small as $128 \times 128$ or $256 \times 256$. However, for low-level vision tasks, the information of pixels is rather vital and should not be down-scaled or degraded before processing.

<table>
<thead>
<tr>
<th>Methods</th>
<th>Task</th>
<th>#P</th>
<th>Input</th>
<th>Output</th>
<th>MACs(G)</th>
<th>FLOPs(G)</th>
<th>Latency(Platform)</th>
</tr>
</thead>
<tbody>
<tr>
<td>EfficientFormer [59]</td>
<td>Classification</td>
<td>37.1M</td>
<td>224 $\times$ 224</td>
<td>-</td>
<td>3.57</td>
<td>-</td>
<td>4.2ms(iPhone 12 NPU)</td>
</tr>
<tr>
<td>LightViT-B [38]</td>
<td>Classification</td>
<td>35.2M</td>
<td>224 $\times$ 224</td>
<td>-</td>
<td>-</td>
<td>3.9</td>
<td>1.2ms(V100 GPU)</td>
</tr>
<tr>
<td>SYENet (Ours)</td>
<td>SR $\times$ 2</td>
<td>4.9K</td>
<td>960 $\times$ 540</td>
<td>1920 $\times$ 1080</td>
<td>-</td>
<td>2.83</td>
<td>16.5ms(Qualcomm Snapdragon mobile SoC)</td>
</tr>
</tbody>
</table>

Table 8: It is obviously shown from the comparison between SYENet and efficient networks for high-level vision task that low-level vision task is more challenging as it requires much fewer parameters to achieve real-time inference due to the large input and output size.

Two typical examples of efficient high-level vision networks are EfficientFormer [59] and LightViT [38]. With 37.1 million parameters, EfficientFormer Supernet could still achieve an extremely low latency of 4.2ms on iPhone 12 NPU. While, as shown in Table 8, our proposed method with only 4.9k parameters could only reach a latency of 16.5ms on Qualcomm Snapdragon mobile SoC due to the large input and output size.

G.2. Constraints of small memory bandwidth of mobile devices

Information Multi-distillation Block IMDB [39] is widely utilised in the efficient SR challenges [50] by many participators as well as the winning teams in some years. However, the structure of IMDB involves a serious disadvantage for mobile devices as it requires the network to store the results generated by each step which should be concatenated after all the steps are done. Therefore, during the inference, once the memory is too small, and consequently data transfer between the memory and disk occurs, the latency shall be seriously enlarged. Thus, keeping memory usage small is another challenge for low-level vision tasks. This is also the reason that the structure of IMDB is not employed in SYENet.

G.3. The influence of network depth towards network latency

The tradeoff between the network depth and kernel size is shown in Table 9. Apparently, one $5 \times 5$ convolution requires more numerical calculations than two $3 \times 3$ convolutions. However, as indicated by Table 9, deeper networks have more data transfer operations between DRAM and local SRAM when the feature map size is too huge. Thus, the larger depth will also enlarge the latency as well. Sometimes, the influence of depth could be even larger than FLOPs in certain platforms due to the
inconsistency between hardware acceleration policies and network structures. Therefore, taking the hardware acceleration policies into consideration is also vital for designing the network structure.

<table>
<thead>
<tr>
<th>Methods</th>
<th>PSNR</th>
<th>Latency(ms)</th>
<th>MAIISP Score</th>
</tr>
</thead>
<tbody>
<tr>
<td>CONV$_{5\times5}$</td>
<td>24.8732</td>
<td>11.4</td>
<td>16.57</td>
</tr>
<tr>
<td>CONV$<em>{3\times3}$ → CONV$</em>{3\times3}$</td>
<td>24.8824</td>
<td>12.9</td>
<td>14.83</td>
</tr>
<tr>
<td>CONV$<em>{3\times3}$ → PReLU → CONV$</em>{3\times3}$</td>
<td><strong>24.9349</strong></td>
<td>13.4</td>
<td>15.35</td>
</tr>
</tbody>
</table>

Table 9: Depth-kernel size tradeoff: tested on MAI ISP dataset

**H. Disadvantage of SYENet on VSR task**

The experiment results of SYENet on Video SR problems are very unsatisfactory, which is even worse than that of SISR. As suggested by BasicVSR [6], the VSR network should have four modules which are propagation, alignment, aggregation, and upsampling. Since SYENet is too small to implement the propagation and alignment module, SYENet could not utilise the information of neighboring frames to enhance the VSR performance. In fact, the neighboring frames injected into SYENet actually impair the restoration as they provide misaligned pixel information that SYENet cannot make good use of.

**I. More low-light enhancement comparisons**

The images are shown in Fig. 9.

**J. More image signal processing comparisons**

The images are shown in Fig. 10.

**K. More Feature maps by each branch**

The images are shown in Fig. 11.
Figure 9: Low-light enhancement comparisons
Figure 10: Image signal processing comparisons
Figure 11: Feature maps of each branch and fused result