

AMC: AutoML for Model Compression and Acceleration on Mobile Devices

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Abstract. Model compression is an effective technique to efficiently deploy neural network models on mobile devices which have limited computation resources and tight power budgets. Conventional model compression techniques rely on *hand-crafted* features and require domain experts to explore the large design space trading off among model size, speed, and accuracy, which is usually sub-optimal and time-consuming. In this paper, we propose AutoML for Model Compression (AMC) which leverages reinforcement learning to efficiently sample the design space and can improve the model compression quality. We achieved state-of-the-art model compression results in a fully automated way *without any human efforts*. Under 4× FLOPs reduction, we achieved **2.7%** better accuracy than the hand-crafted model compression method for VGG-16 on ImageNet. We applied this automated, push-the-button compression pipeline to MobileNet-V1 and achieved a speedup of **1.53×** on the GPU (Titan Xp) and **1.95×** on an Android phone (Google Pixel 1), with negligible loss of accuracy.

Keywords: AutoML · Reinforcement learning · Model compression · CNN acceleration · Mobile vision.

1 Introduction

In many machine learning applications (*e.g.*, robotics, self-driving cars, and advertisement ranking), deep neural networks are constrained by latency, energy and model size budget. Many works have been proposed to improve the hardware efficiency of neural networks by model compression [26, 19, 22]. The core of model compression technique is to determine the compression policy for each layer as they have different redundancy, which conventionally requires hand-crafted heuristics and domain experts to explore the large design space trading off among model size, speed, and accuracy. The design space is so large that human heuristic is usually sub-optimal, and manual model compression is time-consuming. To this end, we aim to automatically find the compression policy for an arbitrary network to achieve even better performance than human strategy.

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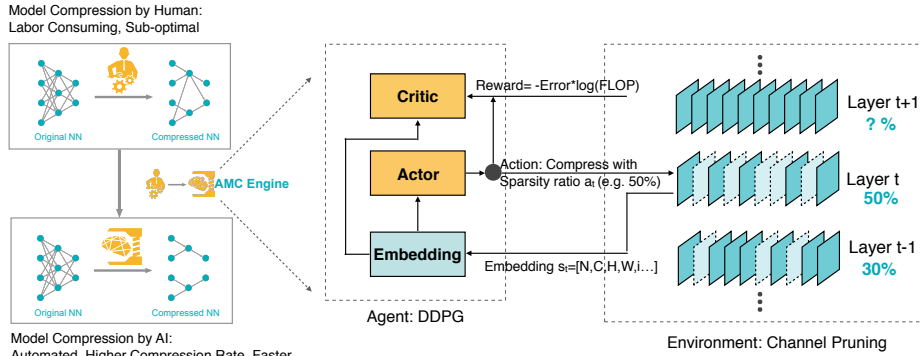


Fig. 1. Overview of AutoML for Model Compression (AMC) engine. Left: AMC replaces human and makes model compression fully automated while performing better than human. Right: Form AMC as a reinforcement learning problem. We process a pretrained network (*e.g.*, MobileNet-V1) in a layer-by-layer manner. Our reinforcement learning agent (DDPG) receives the embedding s_t from a layer t , and outputs a sparsity ratio a_t . After the layer is compressed with a_t , it moves to the next layer L_{t+1} . The accuracy of the pruned model with all layers compressed is evaluated. Finally, as a function of accuracy and FLOP, reward R is returned to the reinforcement learning agent.

As the layers in deep neural networks are correlated in an unknown way, determining the compression policy is highly non-trivial. The problem also has exponential complexity as the network goes deeper, which is infeasible to be solved by brute-force methods. Reinforcement learning methods have been widely approved to have better sample efficiency than random exploration and achieve better solution. Therefore, we propose AutoML for Model Compression (AMC) which leverages reinforcement learning to efficiently sample the design space and greatly improve the model compression quality. Figure 1 illustrates our AMC engine. When compressing a network, rather than relying on human experience or hand-crafted heuristics, our AMC engine automates this process and frees the compression pipeline from human labor.

We observe that the accuracy of the compressed model is very sensitive to the sparsity of each layer, requiring a fine-grained action space. Therefore, instead of searching over a discrete space, we come up with a continuous compression ratio control strategy with a DDPG [32] agent to learn through trials and errors: penalizing accuracy loss while encouraging model shrinking and speedup. The actor-critic structure also helps to reduce variance, facilitating stabler training. Specifically, our DDPG agent processes the network in a layer-wise manner. For each layer L_t , the agent receives a layer embedding s_t which encodes useful characteristics of this layer, and then it outputs a precise compression ratio a_t . After layer L_t is compressed with a_t , the agent moves to the next layer L_{t+1} . The validation accuracy of the pruned model with all layers compressed is evaluated without fine-tuning, which is an efficient delegate of the fine-tuned accuracy. This

Table 1. Comparisons of reinforcement learning approaches for models searching (NAS: Neural Architecture Search [57], NT: Network Transformation [6], N2N: Network to Network [2], and AMC: AutoML for Model Compression. AMC distinguishes from other works by getting reward without fine-tuning, continuous search space control, and can produce both accuracy-guaranteed and hardware resource-constrained models.

	NAS	NT	N2N	AMC
optimize for accuracy	✓	✓	✓	✓
optimize for latency				✓
simple, non-RNN controller				✓
fast exploration with few GPUs		✓	✓	✓
continuous action space				✓

simple approximation can improve the search time not having to retrain the model, and provide high quality search result.

After the policy search is done, the best-explored model is fine-tuned to achieve the best performance.

We proposed two compression policy search protocols for different scenarios. For *latency-critical* AI applications (*e.g.*, mobile apps, self-driving cars, and advertisement rankings), we propose *resource-constrained* compression to achieve the best accuracy given the maximum amount of hardware resources (*e.g.*, FLOPs, latency, and model size), For *quality-critical* AI applications (*e.g.*, Google Photos) where latency is not a hard constraint, we propose *accuracy-guaranteed* compression to achieve the smallest model with no loss of accuracy.

We achieve *resource-constrained* compression by constraining the search space, in which the action space (pruning ratio) is constrained such that the model compressed by the agent is always below the resources budget. For *accuracy-guaranteed* compression, we define a reward that is a function of both accuracy and hardware resource. With this reward function, we are able to explore the limit of compression without harming the accuracy of models.

To demonstrate the wide and general applicability, we evaluate our AMC engine on multiple neural networks, including VGG [45], ResNet [21], and MobileNet-V1 [23], and we also test the generalization ability of the compressed model from classification to object detection. Extensive experiments suggest that AMC offers better performance than hand-crafted heuristic policies. For ResNet-50, we push the expert-tuned compression ratio [16] from $3.4\times$ to $5\times$ with no loss of accuracy. Furthermore, we reduce the FLOPs of MobileNet-V1 [23] by $2\times$, achieving top one accuracy of 70.2%, which is on a better Pareto curve than 0.75 MobileNet-V1, and we achieve a speedup of $1.53\times$ on the Titan XP and $1.95\times$ on an Android phone.

2 Related Work

CNN Compression and Acceleration. Extensive works [20, 19, 34, 12, 18, 17] have been done on accelerating neural networks by compression. Quantization [55,

10, 41] and special convolution implementations [36, 48, 29, 3] can also speed up the neural networks. Tensor factorization [30, 15, 27, 35] decomposes weights into light-weight pieces, for example [51, 11, 14] proposed to accelerate the fully connected layers with truncated SVD; Jaderberg *et al.* [26] proposed to factorize layers into 1×3 and 3×1 ; and Zhang *et al.* [53] proposed to factorize layers into 3×3 and 1×1 . Channel pruning [40, 24, 1, 38] removes the redundant channels from feature maps. A common problem of these methods is how to determine the sparsity ratio for each layer.

Neural Architecture Search and AutoML. Many works on searching models with reinforcement learning and genetic algorithms [46, 42, 5, 37] greatly improve the performance of neural networks. NAS [57] aims to search the transferable network blocks, and its performance surpasses many *manually* designed architectures [47, 21, 9]. Cai *et al.* [6] proposed to speed up the exploration via network transformation [8]. Inspired by them, N2N [2] integrated reinforcement learning into channel selection. In Table 1, we demonstrate several merits of our AMC engine. Compared with previous work, AMC engine optimizes for both accuracy and latency, requires a simple non-RNN controller, can do fast exploration with fewer GPU hours, and also support continuous action space.

3 Methodology

We present an overview of our AutoML for Model Compression(AMC) engine in Figure 1. We aim to automatically find the redundancy for each layer, characterized by sparsity. We train an reinforcement learning agent to predict the action and give the sparsity, then perform from the pruning. We quickly evaluate the accuracy after pruning but before fine-tuning as an effective delegate of final accuracy. Then we update the agent by encouraging smaller, faster and more accurate models.

3.1 Problem Definition

Model compression is achieved by reducing the number of parameters and computation of each layer in deep neural networks. There are two categories of pruning: fine-grained pruning and structured pruning. *Fine-grained pruning* [19] aims to prune individual unimportant elements in weight tensors, which is able to achieve very high compression rate with no loss of accuracy. However, such algorithms result in an irregular pattern of sparsity, and it requires specialized hardware such as EIE [18] for speed up. *Coarse-grained structured pruning* [31] aims to prune entire regular regions of weight tensors (*e.g.*, channel, row, column, block, *etc.*). The pruned weights are regular and can be accelerated directly with off-the-shelf hardware and libraries. Here we study structured pruning that shrink the input channel of each convolutional and fully connected layer.

Our goal is to precisely find out the effective *sparsity* for each layer, which used to be manually determined in previous studies [38, 31, 22]. Take convolutional layer as an example. The shape of a weight tensor is $n \times c \times k \times k$, where n, c are output and input channels, and k is the kernel size. For fine-grained pruning, the sparsity is defined as the number of zero elements divided by the number of

total elements, *i.e.* $\#zeros/(n \times c \times k \times h)$. For channel pruning, we shrink the weight tensor to $n \times c' \times k \times k$ (where $c' < c$), hence the sparsity becomes c'/c .

3.2 Automated Compression with Reinforcement Learning

AMC leverages reinforcement learning for efficient search over action space. Here we introduce the detailed setting of reinforcement learning framework.

The State Space For each layer t , we have 11 features that characterize the state s_t :

$$(t, n, c, h, w, stride, k, FLOPs[t], reduced, rest, a_{t-1}) \quad (1)$$

where t is the layer index, the dimension of the kernel is $n \times c \times k \times k$, and the input is $c \times h \times w$. $FLOPs[t]$ is the FLOPs of layer L_t . *Reduced* is the total number of reduced FLOPs in previous layers. *Rest* is the number of remaining FLOPs in the following layers. Before being passed to the agent, they are scaled within $[0, 1]$. Such features are essential for the agent to distinguish one convolutional layer from another.

The Action Space Most of the existing works use discrete space as coarse-grained action space (*e.g.*, $\{64, 128, 256, 512\}$ for the number of channels). Coarse-grained action space might not be a problem for a high-accuracy model architecture search. However, we observed that model compression is very sensitive to sparsity ratio and requires fine-grained action space, leading to an explosion of the number of discrete actions (Sec. 4.2). Such large action spaces are difficult to explore efficiently [32]. Discretization also throws away the order: for example, 10% sparsity is more aggressive than 20% and far more aggressive than 30%.

As a result, we propose to use continuous action space $a \in (0, 1]$, which enables more fine-grained and accurate compression.

DDPG Agent As illustrated in Figure 1, the agent receives an embedding state s_t of layer L_t from the environment and then outputs a sparsity ratio as action a_t . The underlying layer is compressed with a_t (rounded to the nearest feasible fraction) using a specified compression algorithm (*e.g.*, channel pruning). Then the agent moves to the next layer L_{t+1} , and receives state s_{t+1} . After finishing the final layer L_T , the reward accuracy is evaluated on the validation set and returned to the agent. For fast exploration, we evaluate the reward accuracy without fine-tuning, which is a good approximation for fine-tuned accuracy (Sec. 4.1).

We use the deep deterministic policy gradient (DDPG) for continuous control of the compression ratio, which is an off-policy actor-critic algorithm. For the exploration noise process, we use truncated normal distribution:

$$\mu'(s_t) \sim \text{TN}(\mu(s_t | \theta_t^\mu), \sigma^2, 0, 1) \quad (2)$$

During exploitation, noise σ is initialized as 0.5 and is decayed after each episode exponentially.

Following Block-QNN [54], which applies a variant form of Bellman’s Equation [50], each transition in an episode is (s_t, a_t, R, s_{t+1}) , where R is the reward after the network is compressed. During the update, the baseline reward b is subtracted to reduce the variance of gradient estimation, which is an exponential moving average of the previous rewards [56, 6]:

$$\begin{aligned} Loss &= \frac{1}{N} \sum_i (y_i - Q(s_i, a_i | \theta^Q))^2 \\ y_i &= r_i - b + \gamma Q(s_{i+1}, \mu(s_{i+1}) | \theta^Q) \end{aligned} \quad (3)$$

The discount factor γ is set to 1 to avoid over-prioritizing short-term rewards [4].

3.3 Search Protocols

Resource-Constrained Compression By limiting the action space (the sparsity ratio for each layer), we can accurately arrive at the target compression ratio. Following [57, 4, 54], we use the following reward:

$$R_{err} = -Error \quad (4)$$

This reward offers no incentive for model size reduction, so we the achieve target compression ratio by an alternative way: limiting the action space. Take fine-grained pruning for model size reduction as an example: we allow arbitrary action a at the first few layers; we start to limit the action a when we find that the budget is insufficient even after compressing **all** the following layers with most aggressive strategy. Algorithm 1 illustrates the process. (For channel pruning, the code will be longer but similar, since removing input channels of layer L_t will also remove the corresponding output channels of L_{t-1} , reducing parameters/FLOPs of both layers). Note again that our algorithm is not limited to constraining *model size* and it can be replaced by other resources, such as FLOPs or the actual inference time on mobile device. Based on our experiments (Sec. 4.1), as the agent receives no incentive for going below the budget, it can precisely arrive at the target compression ratio.

Accuracy-Guaranteed Compression By tweaking the reward function, we can accurately find out the limit of compression that offers no loss of accuracy. We empirically observe that *Error* is inversely-proportional to $\log(FLOPs)$ or $\log(\#Param)$ [7]. Driven by this, we devise the following reward function:

$$R_{FLOPs} = -Error \cdot \log(FLOPs) \quad (5)$$

$$R_{Param} = -Error \cdot \log(\#Param) \quad (6)$$

This reward function is sensitive to *Error*; in the meantime, it provides a small incentive for reducing FLOPs or model size. Based on our experiments in Figure 4.1, we note that our agent automatically finds the limit of compression.

4 Experimental Results

For fine-grained pruning [19], we prune the weights with least magnitude. The maximum sparsity ratio a_{max} for convolutional layers is set to 0.8, and a_{max} for

Algorithm 1 Predict the sparsity ratio \mathbf{action}_t for layer L_t with constrained model size (number of parameters) using fine-grained pruning

▷ Initialize the reduced model size so far
if t is equal to 0 **then**
 $W_{\text{reduced}} \leftarrow 0$
end if

▷ Compute the agent’s action and bound it with the maximum sparsity ratio
 $\mathbf{action}_t \leftarrow \mu'(s_t)$
 $\mathbf{action}_t \leftarrow \min(\mathbf{action}_t, \mathbf{action}_{\text{max}})$

▷ Compute the model size of the whole model and all the later layers
 $W_{\text{all}} \leftarrow \sum_k W_k$
 $W_{\text{rest}} \leftarrow \sum_{k=t+1} W_k$

▷ Compute the number of parameters we have to reduce in the current layer if all the later layers are pruned with the maximum sparsity ratio. α is the target sparsity ratio of the whole model.
 $W_{\text{duty}} \leftarrow \alpha \cdot W_{\text{all}} - \mathbf{action}_{\text{max}} \cdot W_{\text{rest}} - W_{\text{reduced}}$

▷ Bound \mathbf{action}_t if it is too small to meet the target model size reduction
 $\mathbf{action}_t \leftarrow \max(\mathbf{action}_t, W_{\text{duty}}/W_t)$

▷ Update the accumulation of reduced model size
 $W_{\text{reduced}} \leftarrow W_{\text{reduced}} + \mathbf{action}_t \cdot W_t$

return \mathbf{action}_t

fully connected layer is set to 0.98. For channel pruning, we use *max response* selection (pruning the weights according to the magnitude [20]), and preserve Batch Normalization [25] layers during pruning instead of merging them into convolutional layers. The maximum sparsity ratios a_{max} for all layers are set to 0.8. Note that the manual upper bound a_{max} is only intended for faster search, one can simply use $a_{\text{max}} = 1$ which also produces similar results. Our actor network μ has two hidden layers, each with 300 units. The final output layer is a sigmoid layer to bound the actions within $(0, 1)$. Our critic network Q also had two hidden layers, both with 300 units. Actions are included in the second hidden layer. We use $\tau = 0.01$ for the soft target updates and train the network with 64 as batch size and 2000 as replay buffer size. Our agent first explores 100 episodes with a constant noise $\sigma = 0.5$, and then exploits 300 episodes with exponentially decayed noise σ .

Table 2. Pruning policy comparison of Plain-20, ResNets [21] on CIFAR-10 [28]. R_{Err} corresponds to FLOPs-constrained compression with channel pruning, while R_{Param} corresponds to accuracy guaranteed compression with fine-grained pruning. For both shallow network Plain-20 and deeper network ResNets, AMC outperforms *hand-crafted* policies by a large margin. This enables efficient exploration without fine-tuning. Although AMC makes many trials on model architecture, we have separate validation and test dataset. No over-fitting is observed.

Model	Policy	Ratio	Val Acc.	Test Acc.	Acc. after FT.
Plain-20 (90.5%)	deep (handcraft)	50% FLOPs	79.6	79.2	88.3
	shallow (handcraft)		83.2	82.9	89.2
	uniform (handcraft)		84.0	83.9	89.7
	AMC (R_{Err})		86.4	86.0	90.2
ResNet-56 (92.8%)	uniform (handcraft)	50% FLOPs	87.5	87.4	89.8
	deep (handcraft)		88.4	88.4	91.5
	AMC (R_{Err})		90.2	90.1	91.9
ResNet-50 (93.53%)	AMC (R_{Param})	60% Params	93.64	93.55	-

4.1 CIFAR-10 and Analysis

We conduct extensive experiments and fully analyze our AMC on CIFAR-10 [28] to verify the effectiveness of the 2 search protocols. CIFAR dataset consists of 50k training and 10k testing 32×32 tiny images in ten classes. We split the training images into 45k/5k train/validation. The accuracy reward is obtained on validation images. Our approach is computationally efficient: the RL can finish searching within **1 hour** on a single GeForce GTX TITAN Xp GPU.

FLOPs-Constrained Compression. We conducted FLOPs-constrained experiments on CIFAR-10 with channel pruning. We compare our approach with three empirical policies [31, 22] illustrated in Figure 2: *uniform* sets compression ratio uniformly, *shallow* and *deep* aggressively prune shallow and deep layers respectively. Based on sparsity distribution of different networks, a different strategy might be chosen.

In Table 2, we show us using reward R_{err} to accurately find the sparsity ratios for pruning 50% for Plain-20 and ResNet-56 [21] and compare it with empirical policies. We outperform empirical policies by a large margin. The best pruning setting found by AMC differs from hand-crafted heuristic (Figure 2). It learns a bottleneck architecture [21].

Accuracy-Guaranteed Compression. By using the R_{Param} reward, our agent can automatically find the limit of compression, with smallest model size and little loss of performance. As shown in Table 2, we compress ResNet-50 with fine-grained pruning on CIFAR-10. The result we obtain has up to 60% compression ratio with even a little higher accuracy on both validation set and test set, which might be in light of the regularization effect of pruning.

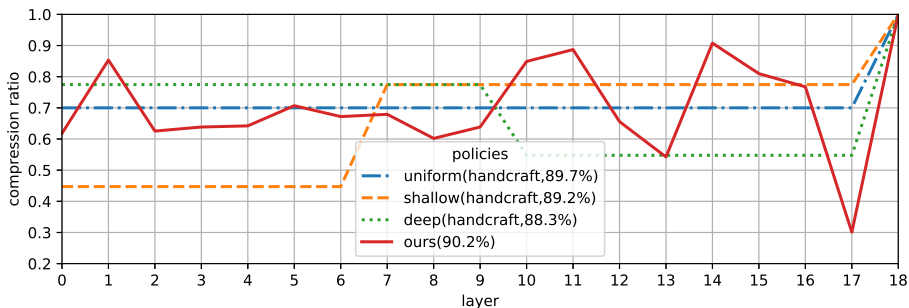


Fig. 2. Comparisons of pruning strategies for Plain-20 under $2\times$. *Uniform* policy sets the same compression ratio for each layer uniformly. *Shallow* and *deep* policies aggressively prune shallow and deep layers respectively. Policy given by AMC looks like sawtooth, which resembles the bottleneck architecture [21]. The accuracy given by AMC outperforms hand-crafted policies. (*better viewed in color*)

Since our reward R_{param} focuses on *Error* and offers very little incentive to compression in the meantime, it prefers the high-performance model with harmless compression. To shorten the search time, we obtain the reward using validation accuracy without fine-tuning. We believe if reward were fine-tuned accuracy, the agent should compress more aggressively, because the fine-tuned accuracy is much closer to the original accuracy.

Speedup policy exploration. Fine-tuning a pruned model usually takes a very long time. We observe a correlation between the pre-fine-tune accuracy and the post fine-tuning accuracy [20, 22]. As shown in Table 2, policies that obtain higher validation accuracy correspondingly have higher fine-tuned accuracy. This enables us to predict final model accuracy without fine-tuning, which results in an efficient and faster policy exploration.

The validation set and test set are separated, and we only use the validation set to generate reward during reinforcement learning. In addition, the compressed models have fewer parameters. As shown in Table 2, the test accuracy and validation accuracy are very close, indicating no over-fitting.

4.2 ImageNet

On ImageNet, we use 3000 images from the **training set** to evaluate the reward function in order to prevent over-fitting. The latency is measured with 224×224 input throughout the experiments.

Push the Limit of Fine-grained Pruning. Fine-grained pruning method prunes neural networks based on individual connections to achieve sparsity in both weights and activations, which is able to achieve higher compression ratio and can be accelerated with specialized hardware such as [18, 17, 39]. However, it requires iterative prune & fine-tune procedure to achieve decent performance [20], and single-shot pruning without retraining will greatly hurt the prediction accuracy

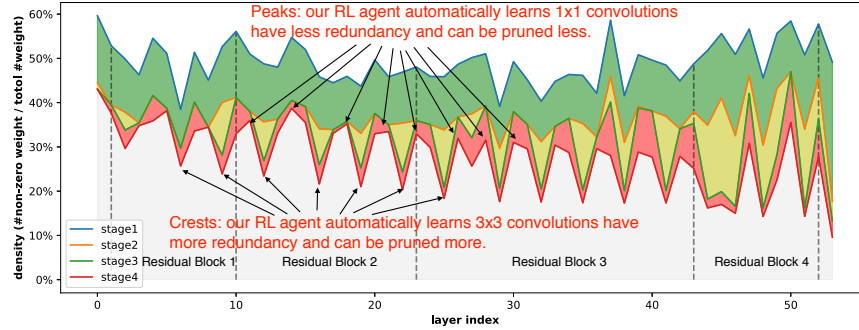


Fig. 3. The pruning policy (sparsity ratio) given by our reinforcement learning agent for ResNet-50. With 4 stages of iterative pruning, we find very salient sparsity pattern across layers: peaks are 1×1 convolution, crests are 3×3 convolution. **The reinforcement learning agent automatically learns that 3×3 convolution has more redundancy than 1×1 convolution and can be pruned more.**

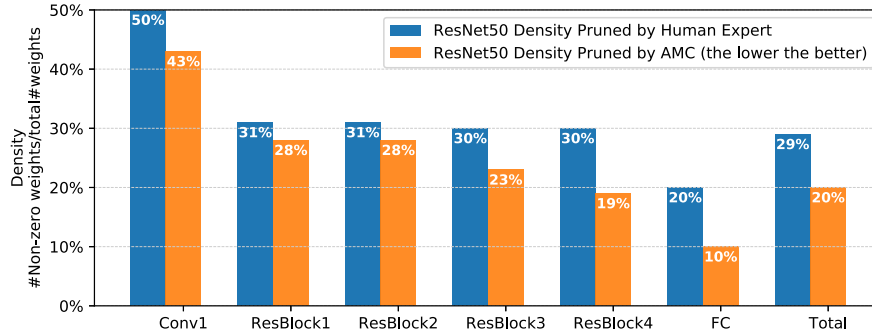


Fig. 4. Our reinforcement learning agent (AMC) can prune the model to a lower density compared with human experts without losing accuracy. (Human expert: $3.4\times$ compression on ResNet50. AMC : $5\times$ compression on ResNet50.)

with large compression rate (say $4\times$), which cannot provide useful supervision for reinforcement learning agent.

To tackle the problem, we follow the settings in [16] to conduct 4-iteration pruning & fine-tuning experiments, where the overall density of the full model is set to [50%, 35%, 25% and 20%] in each iteration. For each stage, we run AMC to determine the sparsity ratio of each layer given the overall sparsity. The model is then pruned and fine-tuned for 30 epochs following common protocol. With that framework, we are able to push the expert-tuned compression ratio of ResNet-50 on ImageNet from $3.4\times$ to $5\times$ (see Figure 4) without loss of performance on ImageNet (original ResNet50’s [top-1, top-5] accuracy=[76.13%, 92.86%]; AMC pruned model’s accuracy=[76.11%, 92.89%]). The density of each layer during each stage is displayed in Figure 3. The peaks and crests show that the RL agent automatically learns to prune 3×3 convolutional layers with larger sparsity,

Table 3. Comparison with handcrafted heuristics. AMC improves the performance over human heuristics. (For reference, the baseline VGG-16 has 70.5% top-1 and 89.9% top-5 accuracy; the baseline MobileNet-V1 has 70.9% top-1 and 89.9% top-5 accuracy; the baseline MobileNet-V2 has 71.8% top-1 and 91% top-5 accuracy).

	policy	FLOPs	Δacc %
VGG-16	FP (handcraft) [31]	20%	-14.6
	RNP (handcraft) [33]		-3.58
	SPP (handcraft) [49]		-2.3
	CP (handcraft) [22]		-1.7
	AMC (ours)		-1.4
MobileNet V1	uniform (0.75-224) [23]	56%	-2.5
	AMC (ours)	50%	-0.4
	uniform (0.75-192) [23]	41%	-3.7
	AMC (ours)	40%	-1.7
MobileNet V2	uniform (0.75-224) [44]	50%	-2.0
	AMC (ours)		-1.0

since they generally have larger redundancy; while prunes more compact 1×1 convolutions with lower sparsity. The density statistics of each block is provided in Figure 4. We can find that the density distribution of AMC is quite different from human expert’s result shown in Table 3.8 of [16], suggesting that AMC can fully explore the design space and allocate sparsity in a better way.

Comparison with Heuristic Channel Reduction. Here we compare AMC with existing state-of-the-art channel reduction methods: FP [31], RNP [33] and SPP [49]. All the methods proposed a heuristic strategy to design the pruning ratio of each layer. FP [31] proposed a sensitive analysis scheme to estimate the sensitivity of each layer by evaluating the accuracy with single layer pruned. Layers with lower sensitivity are pruned more aggressively. Such method assumes that errors of different pruned layers can be summed up linearly, which does not stand according to our experiments. RNP [33] groups all convolutional channels into 4 sets and trains a RL agent to decide on the 4 sets according to input image. However, the action space is very rough (only 4 actions for each layer), and it cannot reduce the model size. SPP [49] applies PCA analysis to each layer and takes the reconstruction error as the sensitivity measure to determine the pruning ratios. Such analysis is conducted based on one single layer, failing to take the correlations between layers into consideration. We also compare our method to the original channel pruning paper (CP in Table 3), in which the sparsity ratios of pruned VGG-16 [45] are carefully tuned by human experts (conv5 are skipped, sparsity ratio for conv4 and remaining layers is 1 : 1.5). The results of pruned VGG-16 are presented in Table 3. Consistent with our CIFAR-10 experiments (Sec. 4.1), AMC outperforms all heuristic methods by more than **0.9%**, and beats human expert by **0.3%** without any human labor.

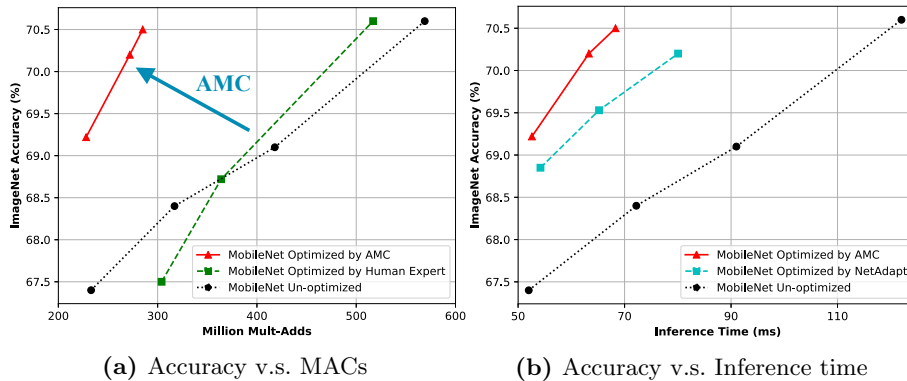


Fig. 5. (a) Comparing the accuracy and MAC trade-off among AMC, human expert, and unpruned MobileNet-v1. AMC strictly dominates human expert in the pareto optimal curve. (b) Comparing the accuracy and latency trade-off among AMC, NetAdapt, and unpruned MobileNet-V1. AMC significantly improves the pareto curve of MobileNet-V1. Reinforcement-learning based AMC surpasses heuristic-based NetAdapt on the pareto curve (inference time both measured on Google Pixel 1).

Apart from VGG-16, we also test AMC on modern efficient neural networks MobileNet-V1 [23] and MobileNet-V2 [44]. Since the networks have already been very compact, it is much harder to further compress them. The easiest way to reduce the channels of a model is to use uniform channel shrinkage, *i.e.* use a width multiplier to uniformly reduce the channels of each layer with a fixed ratio. Both MobileNet-V1 and MobileNet-V2 present the performance of different multiplier and input sizes, and we compare our pruned result with models of same computations. The format are denoted as *uniform (depth multiplier - input size)*. We can find that our method consistently outperforms the uniform baselines. Even for the current state-of-the-art efficient model design MobileNet-V2, AMC can still improve its accuracy by 1.0% at the same computation (Table 3). The pareto curve of MobileNet-V1 is presented in Figure 5a.

Speedup Mobile Inference. Mobile inference acceleration has drawn people’s attention in recent years. Not only can AMC optimize FLOPs and model size, it can also optimize the inference latency, directly benefiting mobile developers. For all mobile inference experiments, we use TensorFlow Lite framework for timing evaluation.

We prune MobileNet-V1 [23], a highly compact network consisting of depth-wise convolution and point-wise convolution layers, and measure how much we can improve its inference speed. Previous attempts using *hand-crafted* policy to prune MobileNet-V1 led to significant accuracy degradation [31]: pruning MobileNet-V1 to 75.5% original parameters results in 67.2% top-1 accuracy*, which is even worse than the original 0.75 MobileNet-V1 (61.9% parameters with

* <http://machinethink.net/blog/compressing-deep-neural-nets/>

Table 4. AMC speeds up MobileNet-V1. Previous attempts using *hand-crafted* policy to prune MobileNet-V1 lead to significant accuracy degradation [31] while AMC pruned models well preserve the accuracy. On NVIDIA Titan XP GPU AMC achieves $1.53\times$ speedup with batch size 50; On Google Pixel-1 CPU, AMC achieves $1.95\times$ speedup with batch size one, while saving the memory by 34%. The AMC pruned MobileNet is not only faster but also more accurate than 0.75 MobileNet. The image size is 224×224 for all experiments and no quantization is applied for apple-to-apple comparison.

	Million MAC	top-1 acc. (%)	top-5 acc. (%)	GPU		Android				
				lat. (ms)	speed (ms)	conv. (ms)	depth. (ms)	total (ms)	speed	memory
1.0 MobileNet-V1	569	70.9	89.5	0.46	2191 fps (1 \times)	110.4	12.8	123.3	8.1 fps (1 \times)	20.1MB
0.75 MobileNet-V1	325	68.4	88.2	0.34	2944 fps (1.34 \times)	62.5	9.7	72.3	13.8 fps (1.71 \times)	14.8MB
0.5 \times FLOPs AMC (ours)	285	70.5	89.3	0.32	3127 fps (1.43\times)	58.8	9.5	68.3	14.6 fps (1.81\times)	14.3MB
0.5 \times Time AMC (ours)	272	70.2	89.2	0.30	3350 fps (1.53\times)	55.0	8.3	63.3	16.0 fps (1.95\times)	13.2MB

68.4% top-1 accuracy). However, our AMC pruning policy significantly improves pruning quality: on ImageNet, AMC-pruned MobileNet-V1 achieved 70.5% Top-1 accuracy with 285 MFLOPs, compared to the original 0.75 MobileNet-V1’s 68.4% Top-1 accuracy with 325 MFLOPs. As illustrated in Figure 5a, human expert’s hand-crafted policy achieves slightly worse performance than that of the original MobileNet-V1 under $2\times$ FLOPs reduction. However, with AMC, we significantly raise the pareto curve, improving the accuracy-MACs trade-off of original MobileNet-V1.

By substituting FLOPs with latency, we can change from FLOPs-constrained search to latency-constrained search and directly optimize the inference time. Our experiment platform is Google Pixel 1 with Qualcomm Snapdragon 821 SoC. As shown in Figure 5b, we greatly reduce the inference time of MobileNet-V1 under the same accuracy. We also compare our learning based policy with a heuristic-based policy [52], and AMC better trades off accuracy and latency. Furthermore, since AMC uses the validation accuracy before fine-tuning as the reward signal while [52] needs local fine-tuning after each step, AMC is more sampling-efficient, requiring fewer GPU hours for policy search.

We show the detailed statistics of our pruned model in Table 4. Model searched with $0.5\times$ FLOPs and $0.5\times$ inference time are profiled and displayed. For $0.5\times$ FLOPs setting, we achieve $1.81\times$ speed up on a Google Pixel 1 phone, and for $0.5\times$ FLOPs setting, we accurately achieve $1.95\times$ speed up, which is very close to actual $2\times$ target, showing that AMC can directly optimize inference time and achieve accurate speed up ratio. We achieve $2.01\times$ speed up for 1×1 convolution but less significant speed up for depth-wise convolution due to the small computation to communication ratio. AMC compressed models also consumes less memory. On GPUs, we also achieve up to $1.5\times$ speedup, less than

Table 5. Compressing Faster R-CNN with VGG16 on PASCAL VOC 2007. Consistent with classification task, AMC also results in better performance under the same compression ratio on object detection task.

	mAP (%)	mAP [0.5, 0.95] (%)
baseline	68.7	36.7
2× handcrafted [22]	68.3 (-0.4)	36.7 (-0.0)
4× handcrafted [22]	66.9 (-1.8)	35.1 (-1.6)
4× handcrafted [53]	67.8 (-0.9)	36.5 (-0.2)
4× AMC (ours)	68.8 (+0.1)	37.2 (+0.5)

mobile phone, which is because a GPU has higher degree of parallelism than a mobile phone.

Generalization Ability. We evaluate the generalization ability of AMC on PASCAL VOC object detection task [13]. We use the compressed VGG-16 (from Sec 4.2) as the backbone for Faster R-CNN [43]. In Table 5, AMC achieves **0.7%** better mAP[0.5, 0.95]) than the best hand-crafted pruning methods under the same compression ratio. AMC even surpasses the baseline by **0.5%** mAP. We hypothesize this improvement as that the optimal compression policy found by the RL agent also serves as effective regularization.

5 Conclusion

Conventional model compression techniques use hand-crafted features and require domain experts to explore a large design space and trade off between model size, speed, and accuracy, which is usually suboptimal and labor-consuming. In this paper, we propose AutoML for Model Compression (AMC), which leverages reinforcement learning to automatically search the design space, greatly improving the model compression quality. We also design two novel reward schemes to perform both resource-constrained compression and accuracy-guaranteed compression. Compelling results have been demonstrated for MobileNet-V1, MobileNet-V2, ResNet and VGG on Cifar and ImageNet. The compressed model generalizes well from classification to detection tasks. On the Google Pixel 1 mobile phone, we push the inference speed of MobileNet-V1 from 8.1 fps to 16.0 fps. AMC facilitates efficient deep neural networks design on mobile devices.

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References

1. Anwar, S., Sung, W.: Compact deep convolutional neural networks with coarse pruning. arXiv preprint arXiv:1610.09639 (2016)
2. Ashok, A., Rhinehart, N., Beainy, F., Kitani, K.M.: N2n learning: Network to network compression via policy gradient reinforcement learning. arXiv preprint arXiv:1709.06030 (2017)
3. Bagherinezhad, H., Rastegari, M., Farhadi, A.: Lcnn: Lookup-based convolutional neural network. arXiv preprint arXiv:1611.06473 (2016)
4. Baker, B., Gupta, O., Naik, N., Raskar, R.: Designing neural network architectures using reinforcement learning. arXiv preprint arXiv:1611.02167 (2016)
5. Brock, A., Lim, T., Ritchie, J.M., Weston, N.: Smash: one-shot model architecture search through hypernetworks. arXiv preprint arXiv:1708.05344 (2017)
6. Cai, H., Chen, T., Zhang, W., Yu, Y., Wang, J.: Reinforcement learning for architecture search by network transformation. arXiv preprint arXiv:1707.04873 (2017)
7. Canziani, A., Paszke, A., Culurciello, E.: An analysis of deep neural network models for practical applications. arXiv preprint arXiv:1605.07678 (2016)
8. Chen, T., Goodfellow, I., Shlens, J.: Net2net: Accelerating learning via knowledge transfer. arXiv preprint arXiv:1511.05641 (2015)
9. Chollet, F.: Xception: Deep learning with depthwise separable convolutions. arXiv preprint arXiv:1610.02357 (2016)
10. Courbariaux, M., Bengio, Y.: Binarynet: Training deep neural networks with weights and activations constrained to+ 1 or-1. arXiv preprint arXiv:1602.02830 (2016)
11. Denton, E.L., Zaremba, W., Bruna, J., LeCun, Y., Fergus, R.: Exploiting linear structure within convolutional networks for efficient evaluation. In: Advances in Neural Information Processing Systems. pp. 1269–1277 (2014)
12. Dong, X., Huang, J., Yang, Y., Yan, S.: More is less: A more complicated network with less inference complexity. arXiv preprint arXiv:1703.08651 (2017)
13. Everingham, M., Van Gool, L., Williams, C.K.I., Winn, J., Zisserman, A.: The PASCAL Visual Object Classes Challenge 2007 (VOC2007) Results. <http://www.pascal-network.org/challenges/VOC/voc2007/workshop/index.html>
14. Girshick, R.: Fast r-cnn. In: Proceedings of the IEEE International Conference on Computer Vision. pp. 1440–1448 (2015)
15. Gong, Y., Liu, L., Yang, M., Bourdev, L.: Compressing deep convolutional networks using vector quantization. arXiv preprint arXiv:1412.6115 (2014)
16. Han, S.: Efficient methods and hardware for deep learning, <https://stacks.stanford.edu/file/druid:qf934gh3708/EFFICIENT%20METHODS%20AND%20HARDWARE%20FOR%20DEEP%20LEARNING-augmented.pdf>
17. Han, S., Kang, J., Mao, H., Hu, Y., Li, X., Li, Y., Xie, D., Luo, H., Yao, S., Wang, Y., et al.: Ese: Efficient speech recognition engine with sparse lstm on fpga. In: Proceedings of the 2017 ACM/SIGDA International Symposium on Field-Programmable Gate Arrays. pp. 75–84. ACM (2017)
18. Han, S., Liu, X., Mao, H., Pu, J., Pedram, A., Horowitz, M.A., Dally, W.J.: Eie: efficient inference engine on compressed deep neural network. In: Proceedings of the 43rd International Symposium on Computer Architecture. pp. 243–254. IEEE Press (2016)
19. Han, S., Mao, H., Dally, W.J.: Deep compression: Compressing deep neural networks with pruning, trained quantization and huffman coding. arXiv preprint arXiv:1510.00149 (2015)

20. Han, S., Pool, J., Tran, J., Dally, W.: Learning both weights and connections for efficient neural network. In: *Advances in Neural Information Processing Systems*. pp. 1135–1143 (2015)
21. He, K., Zhang, X., Ren, S., Sun, J.: Deep residual learning for image recognition. In: *Proceedings of the IEEE Conference on Computer Vision and Pattern Recognition*. pp. 770–778 (2016)
22. He, Y., Zhang, X., Sun, J.: Channel pruning for accelerating very deep neural networks. In: *Proceedings of the IEEE Conference on Computer Vision and Pattern Recognition*. pp. 1389–1397 (2017)
23. Howard, A.G., Zhu, M., Chen, B., Kalenichenko, D., Wang, W., Weyand, T., Andreetto, M., Adam, H.: Mobilenets: Efficient convolutional neural networks for mobile vision applications. *arXiv preprint arXiv:1704.04861* (2017)
24. Hu, H., Peng, R., Tai, Y.W., Tang, C.K.: Network trimming: A data-driven neuron pruning approach towards efficient deep architectures. *arXiv preprint arXiv:1607.03250* (2016)
25. Ioffe, S., Szegedy, C.: Batch normalization: Accelerating deep network training by reducing internal covariate shift. *arXiv preprint arXiv:1502.03167* (2015)
26. Jaderberg, M., Vedaldi, A., Zisserman, A.: Speeding up convolutional neural networks with low rank expansions. *arXiv preprint arXiv:1405.3866* (2014)
27. Kim, Y.D., Park, E., Yoo, S., Choi, T., Yang, L., Shin, D.: Compression of deep convolutional neural networks for fast and low power mobile applications. *arXiv preprint arXiv:1511.06530* (2015)
28. Krizhevsky, A., Hinton, G.: Learning multiple layers of features from tiny images (2009)
29. Lavin, A.: Fast algorithms for convolutional neural networks. *arXiv preprint arXiv:1509.09308* (2015)
30. Lebedev, V., Ganin, Y., Rakhuba, M., Oseledets, I., Lempitsky, V.: Speeding-up convolutional neural networks using fine-tuned cp-decomposition. *arXiv preprint arXiv:1412.6553* (2014)
31. Li, H., Kadav, A., Durdanovic, I., Samet, H., Graf, H.P.: Pruning filters for efficient convnets. *arXiv preprint arXiv:1608.08710* (2016)
32. Lillicrap, T.P., Hunt, J.J., Pritzel, A., Heess, N., Erez, T., Tassa, Y., Silver, D., Wierstra, D.: Continuous control with deep reinforcement learning. *arXiv preprint arXiv:1509.02971* (2015)
33. Lin, J., Rao, Y., Lu, J.: Runtime neural pruning. In: *Advances in Neural Information Processing Systems*. pp. 2178–2188 (2017)
34. Luo, J.H., Wu, J., Lin, W.: Thinet: A filter level pruning method for deep neural network compression. *arXiv preprint arXiv:1707.06342* (2017)
35. Masana, M., van de Weijer, J., Herranz, L., Bagdanov, A.D., Alvarez, J.M.: Domain-adaptive deep network compression. In: *The IEEE International Conference on Computer Vision (ICCV)* (Oct 2017)
36. Mathieu, M., Henaff, M., LeCun, Y.: Fast training of convolutional networks through fts. *arXiv preprint arXiv:1312.5851* (2013)
37. Miikkulainen, R., Liang, J., Meyerson, E., Rawal, A., Fink, D., Francon, O., Raju, B., Navruzyan, A., Duffy, N., Hodjat, B.: Evolving deep neural networks. *arXiv preprint arXiv:1703.00548* (2017)
38. Molchanov, P., Tyree, S., Karras, T., Aila, T., Kautz, J.: Pruning convolutional neural networks for resource efficient transfer learning. *CoRR*, abs/1611.06440 (2016)

39. Parashar, A., Rhu, M., Mukkara, A., Puglielli, A., Venkatesan, R., Khailany, B., Emer, J., Keckler, S., Dally, W.J.: Scnn: An accelerator for compressed-sparse convolutional neural networks. In: 44th International Symposium on Computer Architecture (2017)
40. Polyak, A., Wolf, L.: Channel-level acceleration of deep face representations. *IEEE Access* **3**, 2163–2175 (2015)
41. Rastegari, M., Ordonez, V., Redmon, J., Farhadi, A.: Xnor-net: Imagenet classification using binary convolutional neural networks. In: European Conference on Computer Vision. pp. 525–542. Springer (2016)
42. Real, E., Moore, S., Selle, A., Saxena, S., Suematsu, Y.L., Le, Q., Kurakin, A.: Large-scale evolution of image classifiers. arXiv preprint arXiv:1703.01041 (2017)
43. Ren, S., He, K., Girshick, R., Sun, J.: Faster r-cnn: Towards real-time object detection with region proposal networks. In: Advances in neural information processing systems. pp. 91–99 (2015)
44. Sandler, M., Howard, A., Zhu, M., Zhmoginov, A., Chen, L.C.: Inverted residuals and linear bottlenecks: Mobile networks for classification, detection and segmentation. arXiv preprint arXiv:1801.04381 (2018)
45. Simonyan, K., Zisserman, A.: Very deep convolutional networks for large-scale image recognition. arXiv preprint arXiv:1409.1556 (2014)
46. Stanley, K.O., Miikkulainen, R.: Evolving neural networks through augmenting topologies. *Evolutionary computation* **10**(2), 99–127 (2002)
47. Szegedy, C., Liu, W., Jia, Y., Sermanet, P., Reed, S., Anguelov, D., Erhan, D., Vanhoucke, V., Rabinovich, A.: Going deeper with convolutions. In: Proceedings of the IEEE Conference on Computer Vision and Pattern Recognition. pp. 1–9 (2015)
48. Vasilache, N., Johnson, J., Mathieu, M., Chintala, S., Piantino, S., LeCun, Y.: Fast convolutional nets with fbfft: A gpu performance evaluation. arXiv preprint arXiv:1412.7580 (2014)
49. Wang, H., Zhang, Q., Wang, Y., Hu, R.: Structured probabilistic pruning for deep convolutional neural network acceleration. arXiv preprint arXiv:1709.06994 (2017)
50. Watkins, C.J.C.H.: Learning from delayed rewards. Ph.D. thesis, King’s College, Cambridge (1989)
51. Xue, J., Li, J., Gong, Y.: Restructuring of deep neural network acoustic models with singular value decomposition. In: INTERSPEECH. pp. 2365–2369 (2013)
52. Yang, T.J., Howard, A., Chen, B., Zhang, X., Go, A., Sze, V., Adam, H.: Netadapt: Platform-aware neural network adaptation for mobile applications. arXiv preprint arXiv:1804.03230 (2018)
53. Zhang, X., Zou, J., He, K., Sun, J.: Accelerating very deep convolutional networks for classification and detection. *IEEE transactions on pattern analysis and machine intelligence* **38**(10), 1943–1955 (2016)
54. Zhong, Z., Yan, J., Liu, C.L.: Practical network blocks design with q-learning. arXiv preprint arXiv:1708.05552 (2017)
55. Zhu, C., Han, S., Mao, H., Dally, W.J.: Trained ternary quantization. arXiv preprint arXiv:1612.01064 (2016)
56. Zoph, B., Le, Q.V.: Neural architecture search with reinforcement learning. arXiv preprint arXiv:1611.01578 (2016)
57. Zoph, B., Vasudevan, V., Shlens, J., Le, Q.V.: Learning transferable architectures for scalable image recognition. arXiv preprint arXiv:1707.07012 (2017)